

LCD Module Description

□正式规格 Official specifications

项目编号 Project No.	TFT-H043A22 V2
品描述 Description	TFT LCD Module 480(H) x 272(V) Pixels 4.3 Inch TFT LCD
认 签 章: by customer:	

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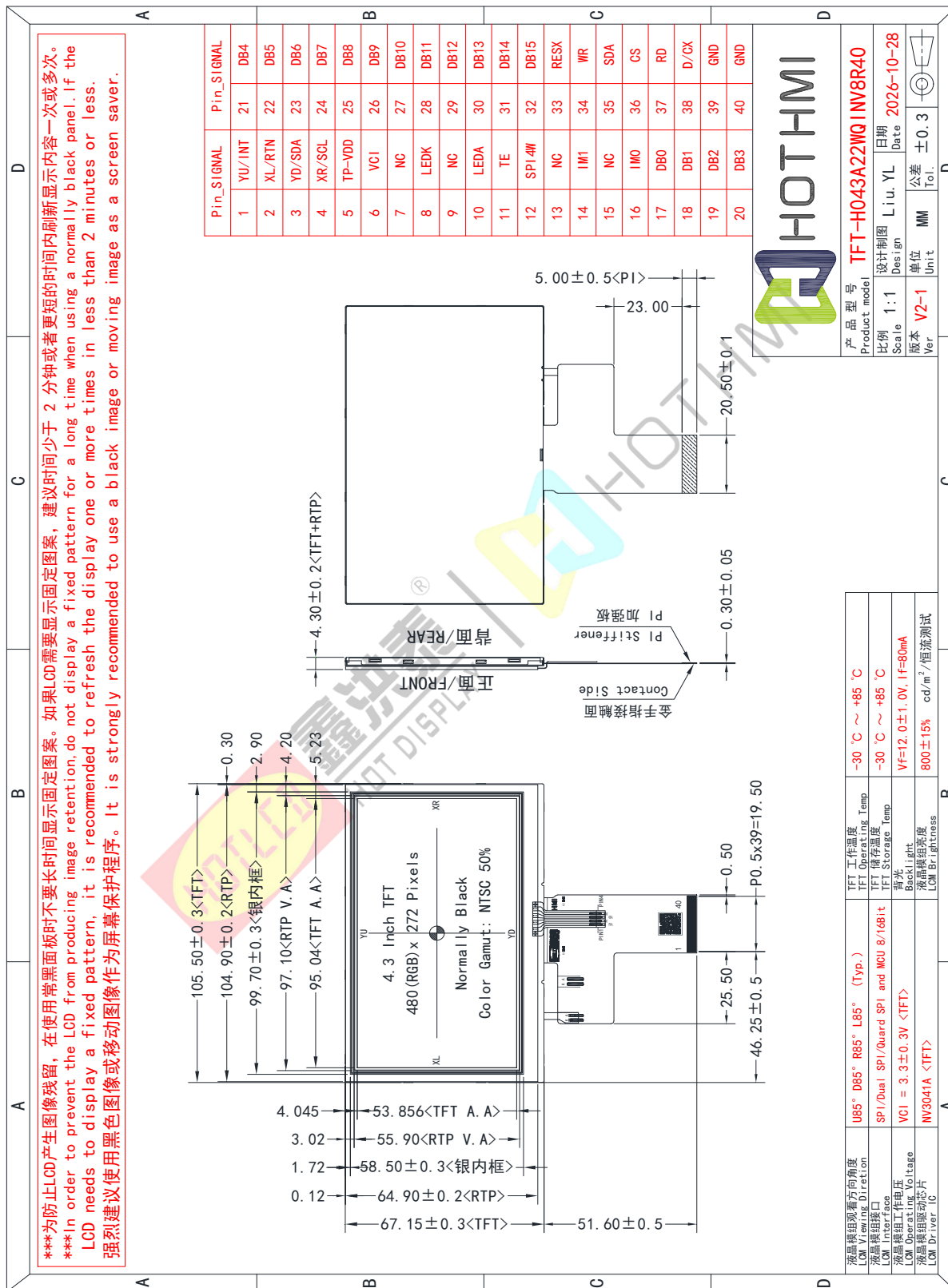
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substance

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一、基本特征 General Feature:

项目 Item	标准值 Standard Value			单位 Unit
TFT 显示尺寸 TFT Display Size	4.3_a-Si TFT_RGB vertical stripe			英寸 Inch
TFT 分辨率 TFT Number of Pixels	480 (H) (H) x 272 (V)			像素 Pixel
TFT 显示有效区域 TFT Display Active Area	95.04 (H) x 53.856 (V) ⑧			毫米 mm
液晶模组外形尺寸 LCD Module Dimensions	105.50 (H) x 67.15 (V) x 2.95 (D) <TFT> 105.50 (H) x 67.15 (V) x 4.30 (D) <TFT+RTP>			毫米 mm
液晶模组观看方向角度 Viewing direction of LCD module	U85° D85° R85° L85° (Typ.)			度数 Deg.
液晶模组接口 LCD Module Interfaces	TFT	■ 3-wire/4-wire SPI ■ ⑧ line/4 line data Quard-SPI ■ MCU 8080 8-bits/16-bits interface		—
	CTP	— — —		
液晶模组芯片 LCD Module Driver IC	TFT	NV3041A <ShangHai, China>		—
	CTP	— — —		
触摸屏 Touch Panel	☒ 不带触摸屏 No TP	☐ 带玻璃盖板 Have Glass Cover	☒ 带电阻触摸屏 Have RTP	—
TFT 模组重量 TFT Module Weight	不带触摸屏 Without TP	T. B. D ±5%		克 g
	带电容触摸屏 With CTP	T. B. D ±5%		
	带电阻触摸屏 With RTP	T. B. D ±5%		

说明 Description:

TFT-H043A22WQ INV8R40_V2-1 <Have RTP_800cd/m²_MCU+SPI 接口>


三、引脚说明 Pin Description

3.1 模组引脚说明 TFT Pin Description

引脚编号 Pin NO.	标号Symbol	详细描述 Description
1	YU/INT	RTP-YU 位置输入, 建议为V1版本产品预留CTP 中断信号 RTP-YU Position Input, Suggest reserving CTP interrupt signal for V1 version products
2	XL/RTN	RTP-XL 位置输入, 建议为V1版本产品预留CTP 复位信号 RTP-XL Position Input, Suggest reserving CTP reset signal for V1 version products
3	YD/SDA	RTP-YD 位置输入, 建议为V1版本产品预留CTP IIC数据信号 RTP-YD Position Input, Suggest reserving CTP IIC data signal for V1 version products
4	XR/SCL	RTP-XR 位置输入, 建议为V1版本产品预留CTP IIC时钟信号 RTP-XR Position Input, Suggest reserving CTP IIC clock signal for V1 version product
5	TP-VDD	不连接, 建议为V1版本产品预留CTP VDD电源 Disconnected, Suggest reserving CTP VDD power supply for V1 version products
6	VCI	TFT 电源 TFT Power supply
7	NC	不连接 Disconnected
8	LEDK	LED阴极 LED cathode
9	NC	不连接 Disconnected
10	LEDA	LED阳极 LED anode
11	TE	撕裂效应输出引脚用于同步 MCU 帧写入, 由 S/W 命令激活。当该引脚未激活时 (TE 功能关闭), 该引脚为 GND 电平。 Tearing effect output pin is used to synchronize MCU frame writing, activated by S/W command. When this pin is not activated (TE function OFF), this pin is GND leve
- 下一页 - - Next Page -		

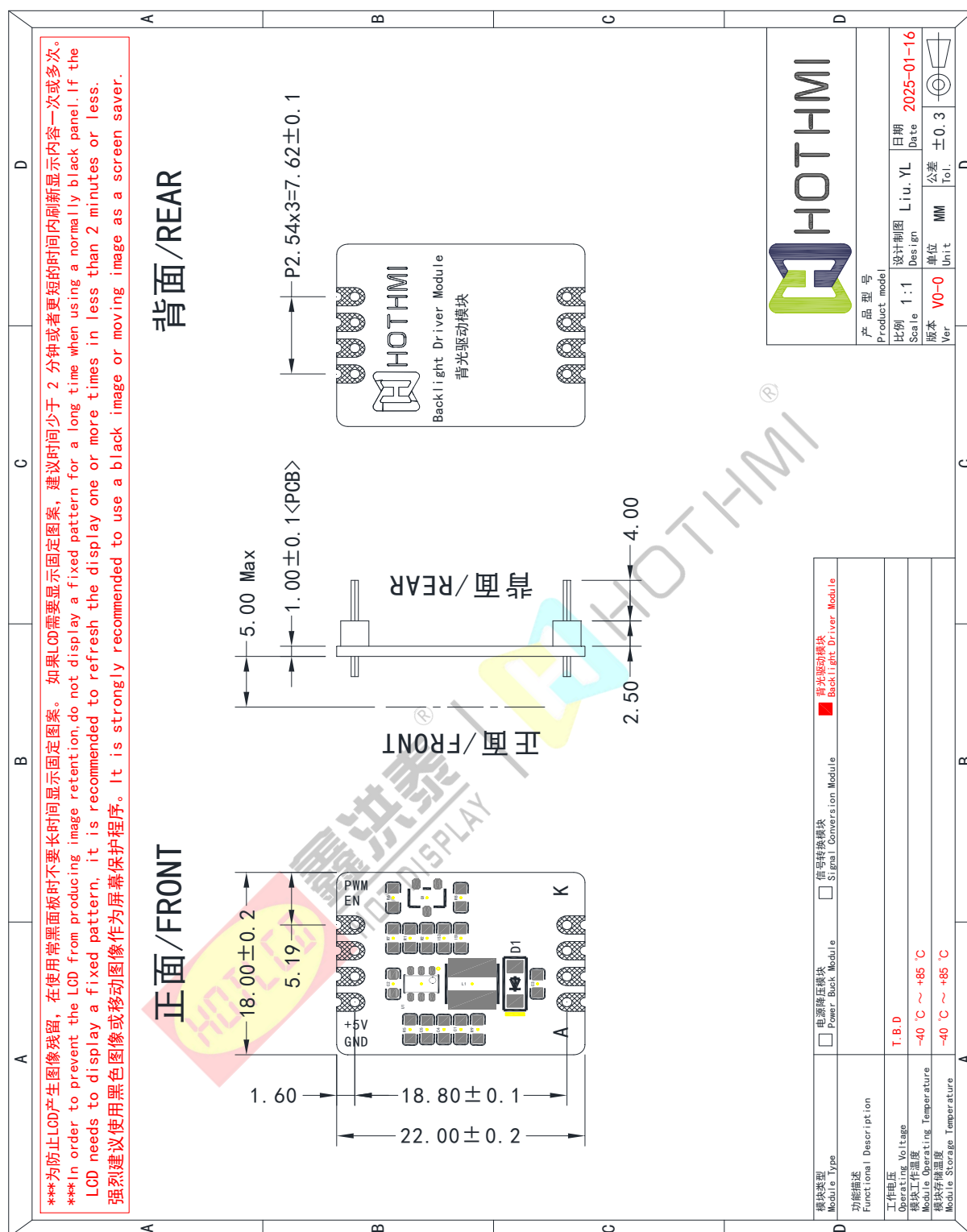
引脚编号 Pin NO.	标号 Symbol	详细描述 Description
12	SPI4W	标准 SPI 3/4线选择 Standard SPI 3/4-wire Select. "SPI4W"="VCI", 4-wire SPI;"SPI4W"="GND", 3-wire SPI
13	NC	不连接 Disconnected
14	IM1	接口选择, 由IM【1 ~ 0】引脚设置 Interface selection, set by IM [1 ~ 0] pins
15	NC	不连接 Disconnected
16	IMO	接口选择, 由IM【1 ~ 0】引脚设置 Interface selection, set by IM [1 ~ 0] pins
17 ~ 32	DB[0~15]	并行接口数据总线 Parallel Interface Data Bus
33	RESX	全局复位。低电平有效, 内部拉高 Global reset. Active low, Internal pull high
34	WR	<ul style="list-style-type: none"> ●在MCU并行接口中写启用 Write enable in the MCU parallel interface ●在SPI接口中时钟输入, 内部拉高 Clock input in SPI interface, pull high internally
35	SPI-SDA	串行通信数据输入和输出, 内部拉低。 Serial communication data input and output, internal pull low.
36	CS	片选, 内部拉高 Chip select, internal pull high
37	RD	在MCU并行接口中读取使能输入, 内部拉高。 Read the enable input in the MCU parallel interface and pull it high internally.
38	D/CX	数据或命令标志, 内部拉高。 Data or Command flag, Internal pull high. "DCX" = "VCI" is data; "DCX" = "GND" is command
39 ~ 40	GND	电源地 Power supply ground
- 结束 - - END -		

温馨提示 1:

Kind reminder1:

3.2 电容触摸引脚说明 CTP Pin Description

引脚编号 Pin NO.	标号 Symbol	详细描述 Description
1	—	未定义 Undefined
2	—	未定义 Undefined
3	—	未定义 Undefined
4	—	未定义 Undefined
5	—	未定义 Undefined
6	—	未定义 Undefined
— 结束 — — END —		



背光模块尺寸

Backlight Module Dimensions

温馨提示：我司所有背光模块长、宽尺寸相同，引脚定义相同。使用的模块以实物为准。

Kind reminder: All of our backlight modules have the same length and width dimensions and the same pin definition. Modules used are subject to physical.

AVDD Boost Module Dimensions

四、电气特性 Electrical Characteristics

4-1 TFT 面板工作条件 TFT Panel Operating Conditions

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
数字电源 Digital Power	VCI	-	Type -0.3	3.3	Type +0.3	伏 V
接口工作电压 IO Supply Voltage	IOVCC	-	Type -0.1	-	Type +0.1	伏 V
模拟电路电源 Power For Analog	AVDD	-	Type -0.3	-	Type +0.3	伏 V
TFT 栅极导通电压 TFT Gate ON Voltage	VGH	-	Type -1.0	-	Type +1.0	伏 V
TFT 栅极关断电压 TFT Gate OFF Voltage	VGL	-	Type -1.0	-	Type +1.0	伏 V
TFT 公共电极电压 TFT Common Electrode Voltage	VCOM	-	Type -0.5	-	Type +0.5	伏 V
工作电流 Operation Current	I _{oc}	VCI=3.3V	22	-	27	毫安 mA
		IOVCC	-	-	-	
待机电流 Standby Current	I _{sc}	STBYB=L	-	0.05	1.5	毫安 mA
TFT 工作温度 TFT Operating Temp	TOPR	-	-30	+25	+85	摄氏度 ℃
TFT 储存温度 TFT Storage Temp	TSTG	-	-30	+25	+85	摄氏度 ℃

温馨提示

1. 电流数据采用 85C1 指针式电流表测量，仅供参考。
2. 必须调节 Vcom 以优化显示质量，如串扰和对比度等。
3. 我们只是建议将设定电压作为参考值。为了获得最佳的显示质量，应根据客户的开发情况改变设定电压。（显示质量可能会因客户的电压设置而改变。）。

Kind reminder:

1. The current data is measured using an 85C1 pointer ammeter, for reference only.
2. Vcom must be adjusted to optimize display quality, as Crosstalk and Contrast Ratio etc.
3. We just kindly recommend the setting-voltages the reference value. In order to get the optimized display quality, the setting-voltage should be changed according to customer's developing condition. (The display quality could be changed by customer's setting - voltage.)

4-2 背光工作条件 LED back light specification

4-2-1: Backlight-1

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
工作电压 Forward voltage	VF	恒流 constant current	Type -1.0	12.0	Type +1.0	伏特 V
工作电流 Forward current	IF		-	80	-	毫安 mA
亮度 (带 LCD) Luminance (With LCD)	Lv	不带触摸 Without TP	Type -15%	1000	Type +15%	坎德拉/平方米 cd/m ²
		带电阻触摸 Have RTP		800		
		带电容触摸 Have CTP		-		
LED 寿命 LED life time	Hr	Ta=25±3 °C	20,000	30,000	-	小时 Hour

不带触摸
No TP带电阻触摸
Have RTP带电容触摸
Have CTP

注释 Note:

1. LED 寿命 (Hr) 定义为在 Ta=25±3 °C, 上表所示的典型电压电流值条件下持续工作直至亮度低于 50% 的时间。

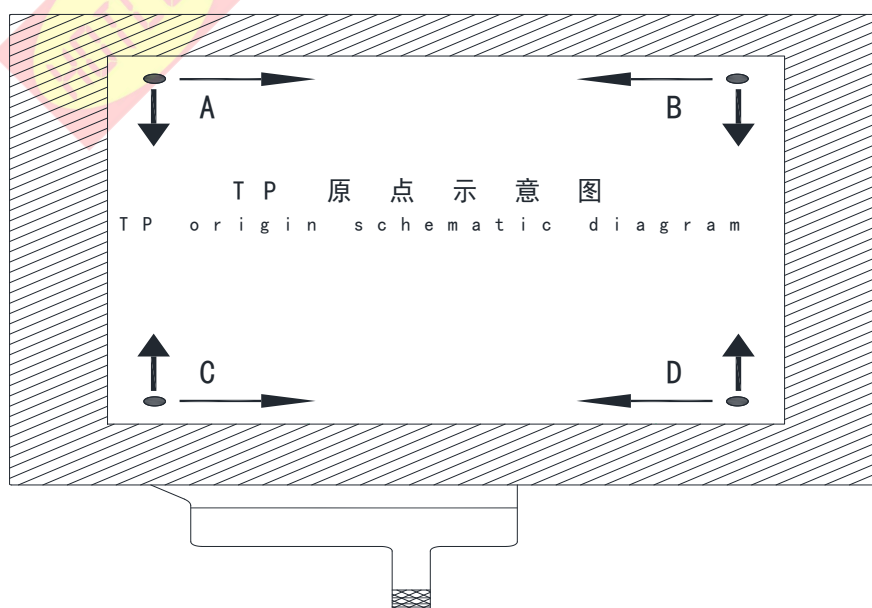
LED life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3 °C, typical IL value indicated in the above table until the brightness becomes less than 50%.

2. 以上结果是按 MTBF 计算方式预估判定的 LED 失效时间, 实际测试 LED 在 Ta=25±3 °C 点亮 5000H, 亮度衰减 8%.

The above results are estimated and judged by the MTBF calculation method of the LED failure time. The actual test LED is lit for 5000H at Ta=25±3 °C, and the brightness decays by 8%.

4-3 电容触摸屏工作条件 CTP Operating Conditions

项目 Item	标号 Symbol	条件 Condition	最小值 Min	典型值 Type	最大值 Max	单位 Unit
表面硬度 Surface hardness	-	铅笔 Pencil	-	6H	-	硬度 Hard
最大点数 maximum point number	-	-	-	-	-	手指 Fingers
坐标原点 origin	-	-	-			位置 Position
X 分辨率 X resolution	-	-	-			-
Y 分辨率 Y resolution	-	-	-			-
透光率 Transmittance	-	-	80	-	-	百分比 %
USB 5V 输入电源 USB 5V input power	VDD5V	-	Type -0.3	-	Type +0.3	伏特 V
数字电源 Digital Power	VDD	-	Type -0.3	3.0	Type +0.3	伏特 V
I/O 电源 I/O Power	IOVDD	-	Type -0.3	-	Type +0.3	伏特 V
工作温度 Operation Temperature	-	20%-90%RH;	-20	+25	+70	摄氏度 ℃
储存温度 Storage Temperature	-		-30	+25	+80	摄氏度 ℃



五、液晶光学规格 TFT OPTICAL SPECIFICATION

光学规格的测试应在暗室（环境亮度1lux，温度=25℃）中使用亮度计系统（测角仪系统和TOPCON BM-5）设备进行测量，测试单元应位于大约在 θ 和 Φ 等于 0 的视角下，距 LCD 表面 50cm 的距离。显示面上测量点的中心应保持固定。测量前背光应工作 30 分钟。

The test of Optical specifications shall be measured in a dark room (ambient luminance 1lux and temperature = 25℃) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0. The center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement.

光学规格表

Optical Specifications Table

参数 Parameter		标号 Symbol	条件 Condition	最小值 Min.	典型值 Typ.	最大值 Max.	单位 Unit	备注 Remark
视角范围 Viewing Angle Range	水平 Horizontal	⊙ 左/L	CR>10	80	85	—	Deg.	Note 1
		⊙ 右/R		80	85	—	Deg.	
	垂直 Vertical	⊙ 上/U		80	85	—	Deg.	
		⊙ 下/D		80	85	—	Deg.	
对比度 Contrast ratio		CR	⊙ = 0°	800	1000	—	—	Note2
色域 Color Gamut		CG	CIE1931	40	50	—	%	
白色色度 White Chromaticity		Wx	⊙ = 0°	-0.03	0.305	+0.03	—	Note4 (Based on C Light)
		Wy			0.336		—	
色彩还原 Reproduction of color	红 Red	Rx			0.603		—	
		Ry			0.307		—	
	绿 Green	Gx			0.314		—	
		Gy			0.557		—	
	蓝 Blue	Bx			0.145		—	
		By			0.153		—	
响应时间（上升 + 下降） Response Time (Rising + Falling)		Tr+Tf	⊙ = 0° Ta= 25℃	—	30	35	ms	Note5

注释 Note:

1. 视角是对比度大于10的角度。视角确定为相对于光轴的水平或3、9点钟方向和垂直或6、12点钟方向 垂直于 LCD 表面（见图 1）。

Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o' clock direction and the vertical or 6, 12 o' clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).

2. 对比度测量应在 $\theta = 0$ 的视角和 LCD 表面的中心进行。亮度测量时, 视场中的所有像素首先设置为白色, 然后设置为暗(黑色)状态。(参见图 1) 亮度对比度 (CR) 是通过数学定义的。

Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (see FIGUR 1) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. 透射率是没有 APF 和没有 CG 的值。

Transmittance is the Value without APF and without CG.

4. 上表中规定的色度坐标应由所有像素首先测量的光谱数据计算为红色、绿色、蓝色和白色。测量应在面板的中心进行。

The color chromaticity coordinates specified in the above table shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

5. 电光响应时间测量应如图 2 所示, 通过打开和关闭“数据”输入信号来进行。亮度从 10%变化到90%所需的时间是 T_r , 90%到10%是 T_f 。

The electro-optical response time measurements shall be made as FIGURE 2 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_f .

Figure1 Measurement Set Up

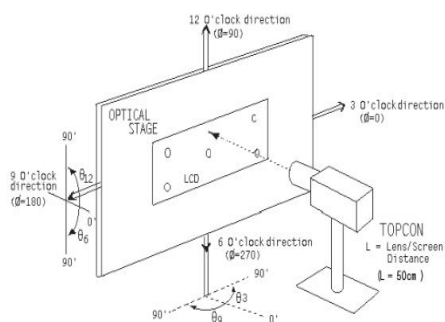


图 1

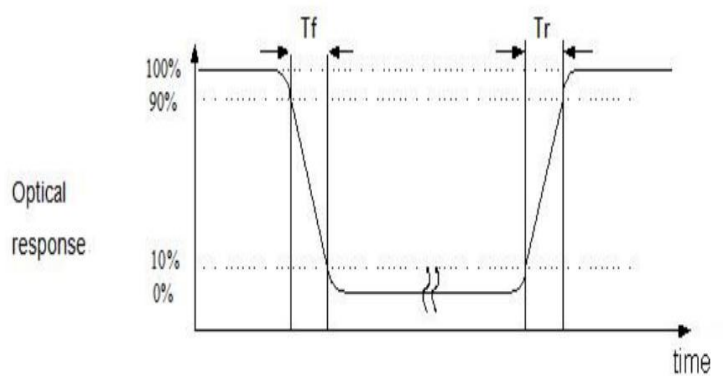
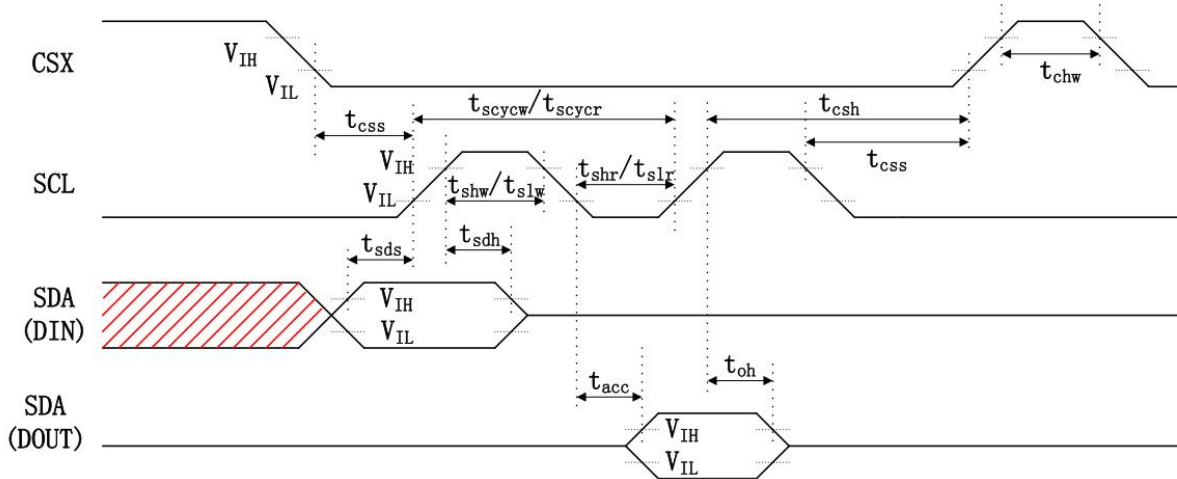


图 2

六、交流特性 AC characteristic

6.1. 串行显示接口 (SPI/Dual-SPI/Quad-SPI)

Display Serial Interface (SPI/Dual-SPI/Quad-SPI)



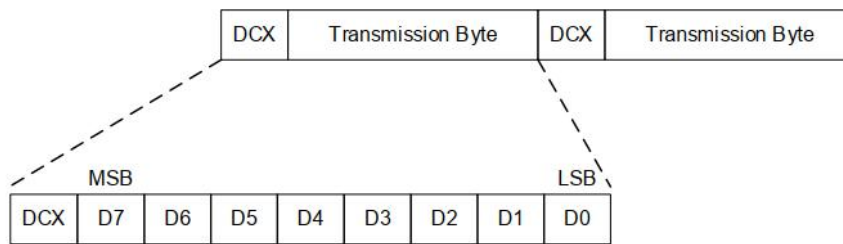
Serial Interface Characteristics

Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
CSX	T_{CSS}	Chip Select Setup Time	10		ns	
	T_{CSH}	Chip Select Hold Time	30		ns	
	T_{CHW}	Chip Select "H" Pulse Width	30		ns	
SCL	T_{SCYCW}	Serial Clock Cycle(Write)	12.5		ns	QSPI 4 lane format (5-6-5): $T_{SCYCW} > 25\text{ns}$ (see "6.4.12.") QSPI 4 lane format (6-6-6): $T_{SCYCW} > 16\text{ns}$ (see "6.4.13") Other Format $T_{SCYCW} > 12.5\text{ns}$
	T_{SHW}	S"L" "H" Pulse Width(Write)	$T_{SCYCW} / 2$		ns	
	T_{SLW}	S"L" "L" Pulse Width(Write)	$T_{SCYCW} / 2$		ns	
	T_{SCYCR}	Serial Clock Cycle(Read)	150		ns	
	T_{SHR}	S"L" "H" Pulse Width(Read)	$T_{SCYCR} / 2$		ns	
	T_{SLR}	S"L" "L" Pulse Width(Read)	$T_{SCYCR} / 2$		ns	
SDA(DIN) / (DOUT)	T_{SDS}	Data Setup Time	5		ns	
	T_{SDH}	Data Hold Time	5		ns	
	T_{ACC}	Access Time	5	50	ns	CLmax=30pF CLmin=8pF
	T_{OH}	Output Disable Time	10		ns	

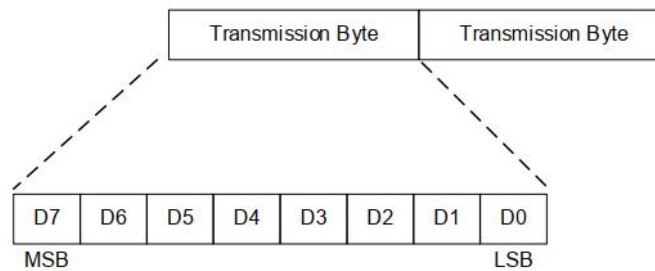
4 wire Serial Interface Characteristics

Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
CSX	T _{CSS}	Chip Select Setup Time	10		ns	
	T _{CSH}	Chip Select Hold Time	30		ns	
	T _{CHW}	Chip Select "H" Pulse Width	30		ns	
SCL	T _{SCYCW}	Serial Clock Cycle(Write)	12.5		ns	
	T _{SHW}	S "L" "H" Pulse Width(Write)	T _{SCYCW} /2		ns	
	T _{SLW}	S "L" "L" Pulse Width(Write)	T _{SCYCW} /2		ns	
	T _{SCYCR}	Serial Clock Cycle(Read)	150		ns	
	T _{SHR}	S "L" "H" "Pulse Width(Read)	T _{SCYCR} /2		ns	
	T _{SLR}	S "L" "L" Pulse Width(Read)	T _{SCYCR} /2		ns	
D/CX	T _{DCS}	D/CX Setup Time	5		ns	
	T _{DCH}	D/CX Hold Time	5		ns	
SDA(DIN) (DOUT)	T _{SDS}	Data Setup Time	5		ns	
	T _{SDH}	Data Hold Time	5		ns	
	T _{ACC}	Access Time	5	50	ns	CLmax=30pF CLmin=8pF
	T _{OH}	Output Disable Time	10		ns	

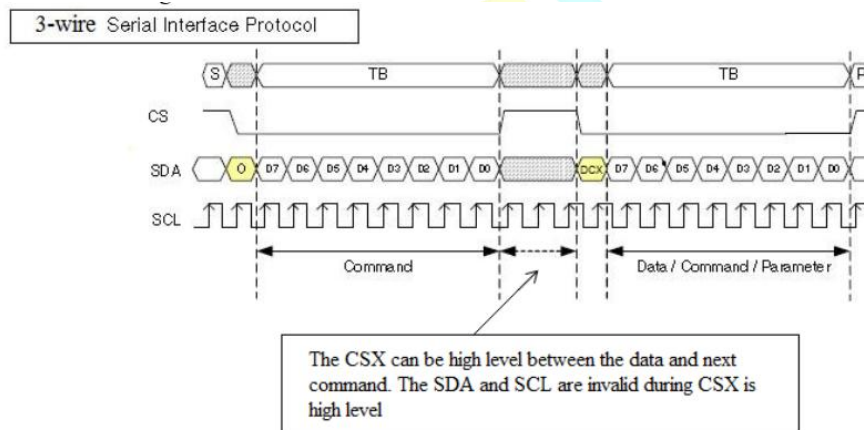
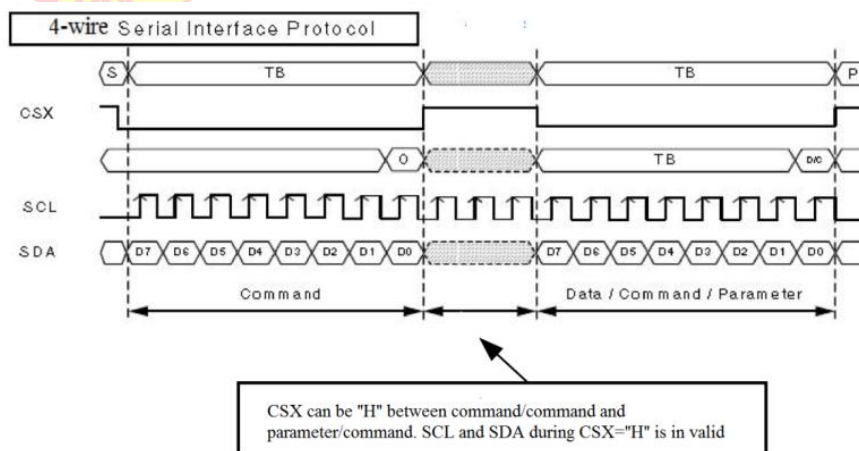
6.1.1. 标准 SPI 接口写入周期序列 Std SPI Interface Write Cycle Sequence



Std-SPI 3-Wire Data flow

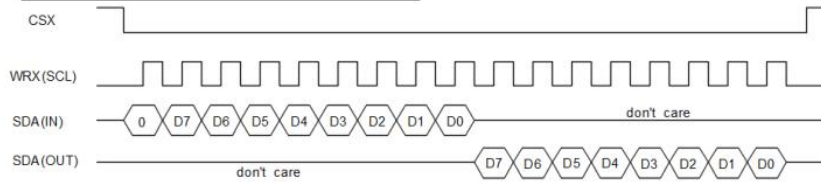


Std-SPI 4-Wire Data flow

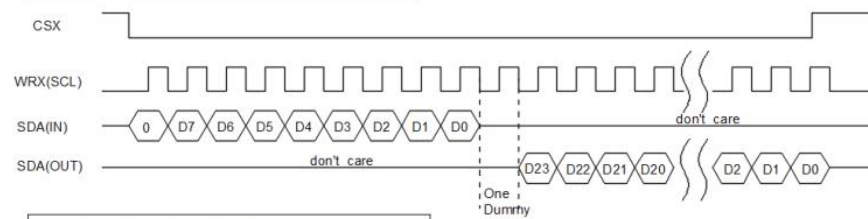


6.1.2. 标准 SPI 接口读取周期序列 Std SPI Interface Read Cycle Sequence

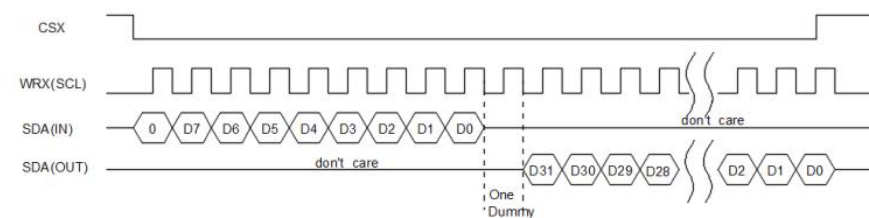
3-wire Serial Interface (Single-byte read)



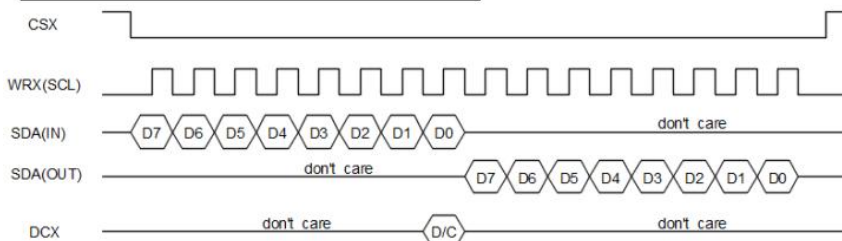
3-wire Serial Interface (3-byte read)



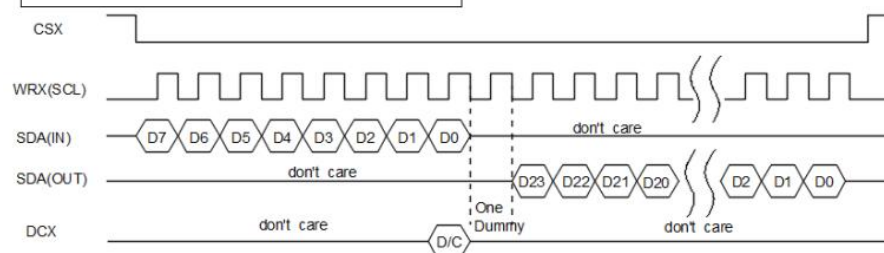
3-wire Serial Interface (4-byte read)



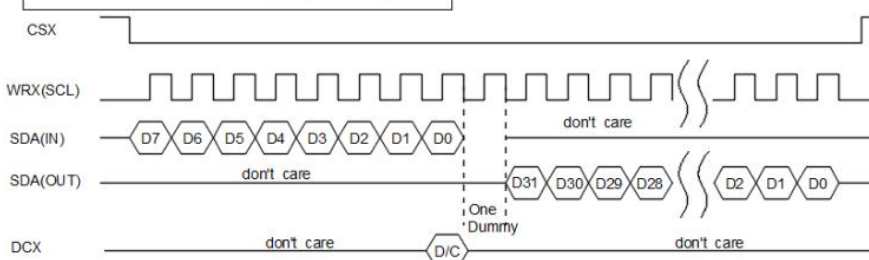
4-wire Serial Interface (Single-byte read)



4-wire Serial Interface (3-byte read)



4-wire Serial Interface (4-byte read)



四数据通道串行接口使用：CSX（芯片启用）、WRX（串行时钟）和 SDA（串行数据输入/输出 1）、DCX（串行数据输出 2）、DB2（串行数据输入 3）和 DB3（串行数据输出 4）。

Four data lane serial interface use: CSX (chip enable), WRX (serial clock) and SDA (serial data input/output 1), DCX (serial data input 2), DB2 (serial data input 3) and DB3 (serial data input 4).

每个传输有三个部分：操作码（CSX 下降沿后的第一个字节）、地址和数据。操作码用于区分 MCU 和 NV3041A 之间的不同操作，如下表所示。

Each transmission has three part: op-code (first byte after CSX falling edge), Address and Data. op-code used to distinguish different operations between MCU and NV3041A, as below table shows.

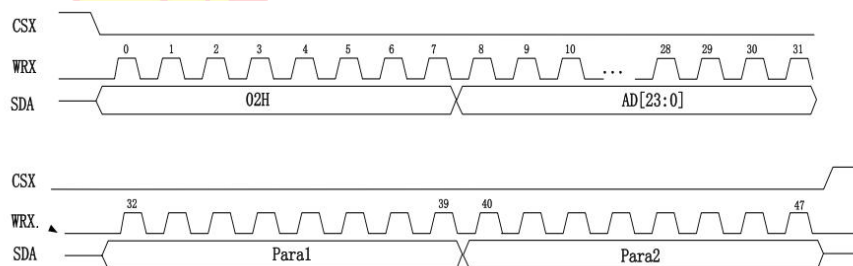
OP code	Operation	Description
02H	Write Command	In general, this operation used to write registers. When the address is "2C", the following data is identified as RAM data. It's not a good choice because of its slowly accessing rate.
03H	Read Command	Read register content from NV3041A
12H	Write RAM data	The address must be "2C" and the timing takes 24 cycles, see the section 6.4.12 and 6.4.13 for details
32H	Write RAM data	The address must be "2C" and the timing takes 6 cycles, see the section 6.4.12 and 6.4.13 for details

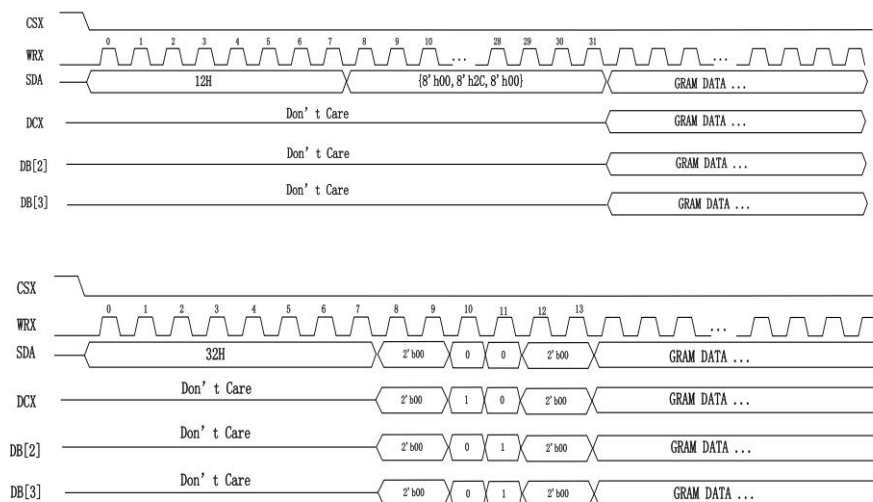
Note: Each transmission must end with CSX rising edge.

6.1.3. Q-SPI 接口写入周期序列 Quad-SPI Interface Write Cycle Sequence

命令写入功能由 CSX、WRX、SDA 驱动，如下所示。操作码“02H”在 CSX 下降沿后发送。AD[23:0] 格式为 {8'h00, CMD[7:0], 8'h00}。如果地址包含“2CH”命令，则 Para# 将被捕获为 GRAM 数据。

The function of command writing is driven by CSX, WRX, SDA, as shown below. op code "02H" is sent after CSX falling edge. AD[23:0] format is {8'h00, CMD[7:0], 8'h00}. If the address includes "2CH" command, Para# is captured as GRAM data.



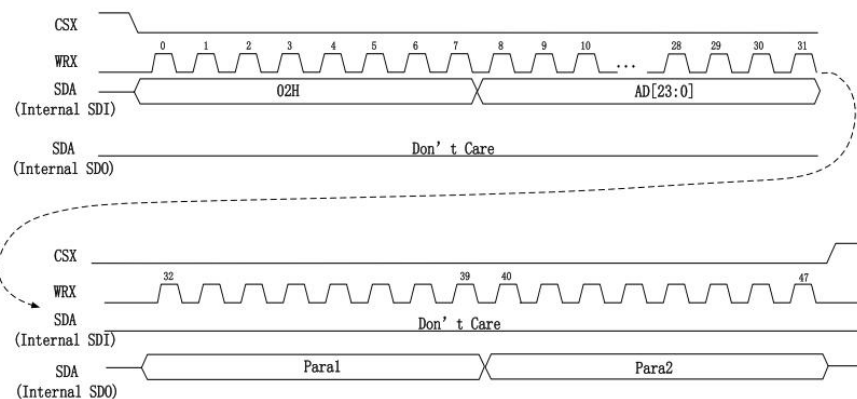


Write GRAM (op code = “12H” or “32H”)

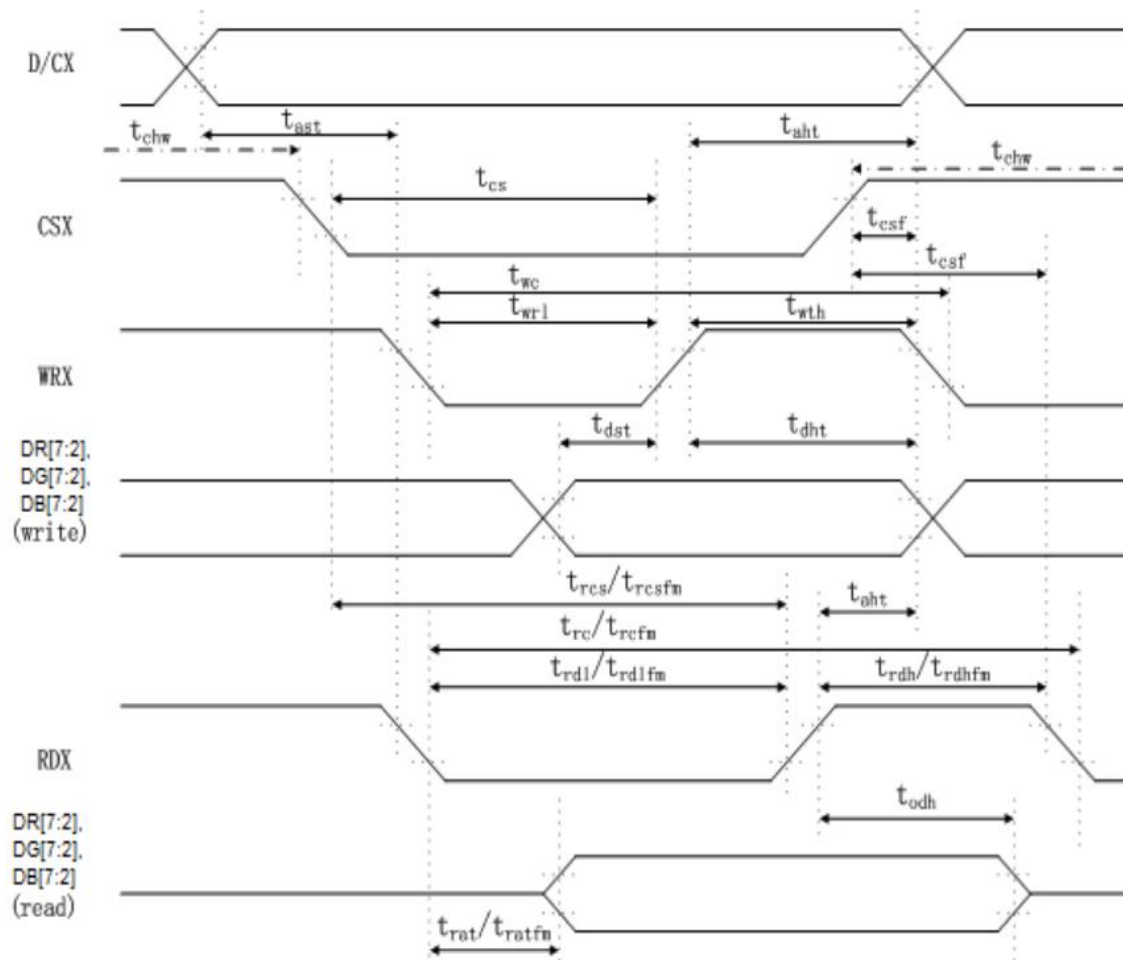
6.1.4. Q-SPI 接口写入周期序列 Quad-SPI Interface Write Cycle Sequence

命令读取功能也由 CSX、WRX、SDA 驱动，如下所示。操作码 “03H” 在 CSX 下降沿后发送。AD[23:0] 格式为 {8'h00, CMD[7:0], 8'h00}。当到达 AD 最后一个周期的 WRX 下降沿时，SDA 方向切换到 NV3041A 驱动的输出。如下图所示。

The function of command reading is also driven by CSX, WRX, SDA, as shown below. op code “03H” is sent after CSX falling edge. AD[23:0] format is {8'h00, CMD[7:0], 8'h00}. SDA direction is switched to output driven by NV3041A, when WRX falling edge of last cycle of AD is arrived. As shown below.



6.2. 并行 MCU 接口 Parallel MCU Interface



Note: Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

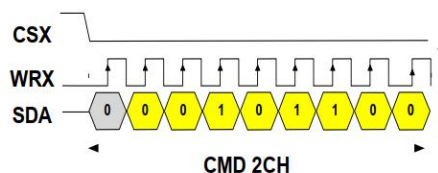
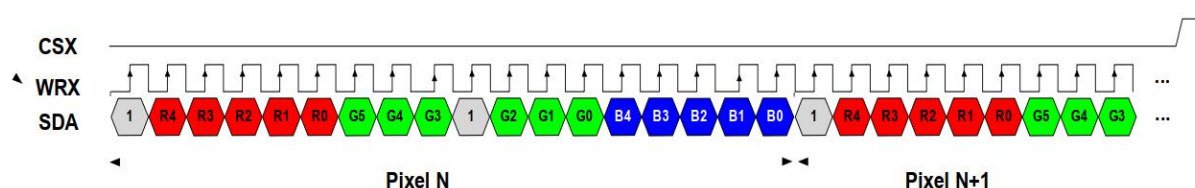
Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
D/CX	T_{AST}	Address Setup Time	0		ns	
	T_{AHT}	Address Hold Time (W/R)	10		ns	
CSX	T_{CHW}	"S" "H" Pulse Width	25		ns	
	T_{CS}	Chip Select Setup Time (W)	10		ns	
	T_{RCS}	Chip Select Setup Time (Read ID)	45		ns	
	T_{RCSFM}	Chip Select Setup Time (Read FM)	355		ns	
	T_{CSF}	Chip Select Wait Time (W/R)	10		ns	

Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
WRX	T _{WC}	Write Cycle	50		ns	MCU 16 Bit Format (5-6-5): T _{wc} >100ns (see “6.4.8.”) MCU 16 Bit Format (6-6-6): T _{wc} >66ns (see “6.4.9.” Figure 6.4.9.4) Other Format T _{wc} >50ns
	T _{WRH}	Control Pulse H Duration	T _{WC} /2		ns	
	T _{WRL}	Control Pulse L Duration	T _{WC} /2		ns	
RDX	T _{RC}	Read Cycle(ID)	160		ns	When Read ID
	T _{RDH}	Control Pulse H Duration(ID)	T _{RC} /2		ns	
	T _{RDL}	Control Pulse L Duration(ID)	T _{RC} /2		ns	
RDX	T _{RFCM}	Read Cycle(FM)	450		ns	When Read From Frame Memory
	T _{RDHFM}	Control Pulse H Duration(FM)	T _{RFCM} /2		ns	
	T _{RDLFM}	Control Pulse L Duration(FM)	T _{RFCM} /2		ns	
DR[7:2], DG[7:2], DB[7:2]	T _{DST}	Data Setup Time	10		ns	CL _{max} =30pF CL _{min} =8pF
	T _{DHT}	Data Hold Time	10		ns	
	T _{RAT}	Read Access Time(ID)		40	ns	
	T _{RATFM}	Read Access Time(FM)		340	ns	
	T _{ODH}	Output Disable Time	20	80	ns	

6.3. 显示数据写入格式 Display Date Writing Format

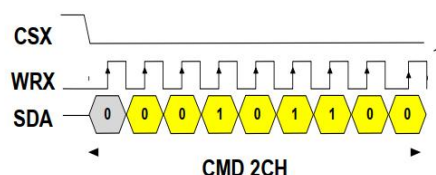
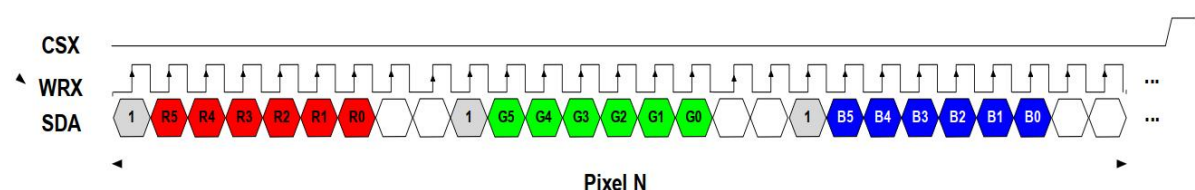
6.3.1. 标准 SPI 3 线 RGB 格式 (5-6-5) Std SPI 3Wire RGB Format (5-6-5)

<1> IM[2:0] = 3'b001
 <2> SPI4W = 0
 <3> Register 3AH = 01h

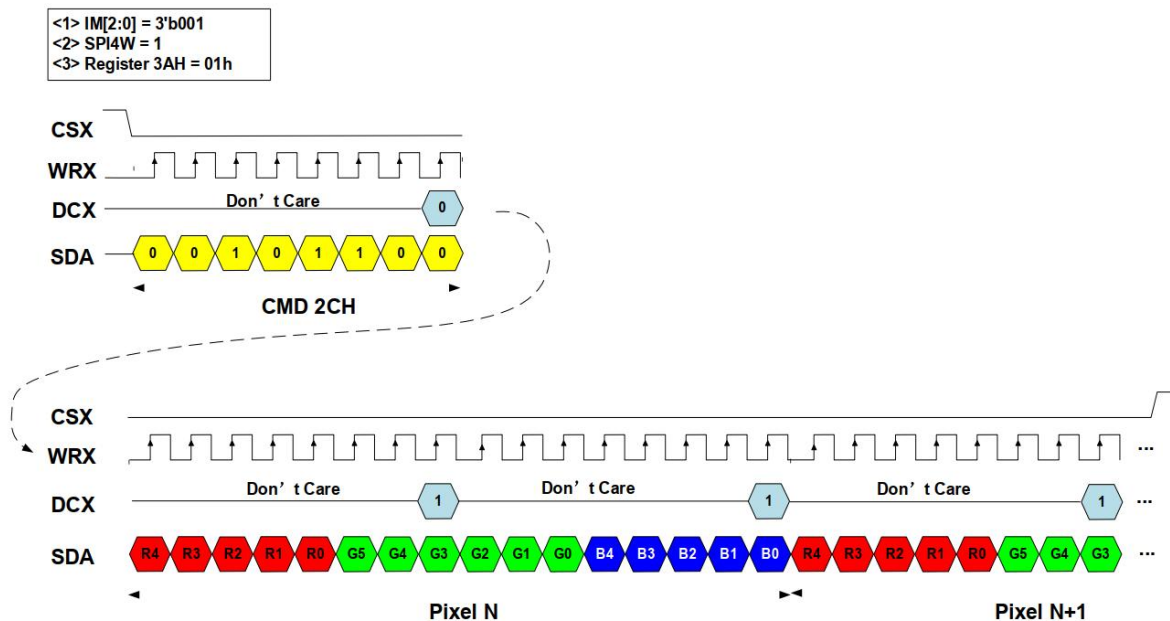


6.3.2. 标准 SPI 3 线 RGB 格式 (6-6-6) Std SPI 3Wire RGB Format (6-6-6)

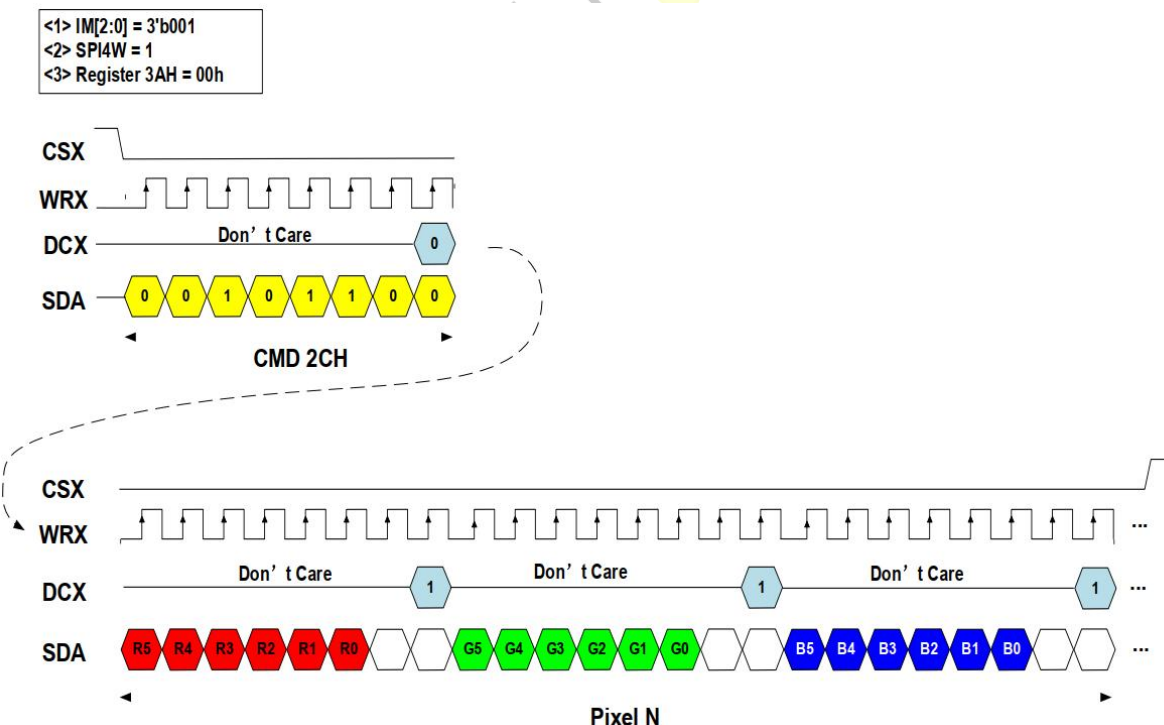
<1> IM[2:0] = 3'b001
 <2> SPI4W = 0
 <3> Register 3AH = 00h



6.3.3. 标准 SPI 4 线 RGB 格式 (5-6-5) Std SPI 4Wire RGB Format (5-6-5)

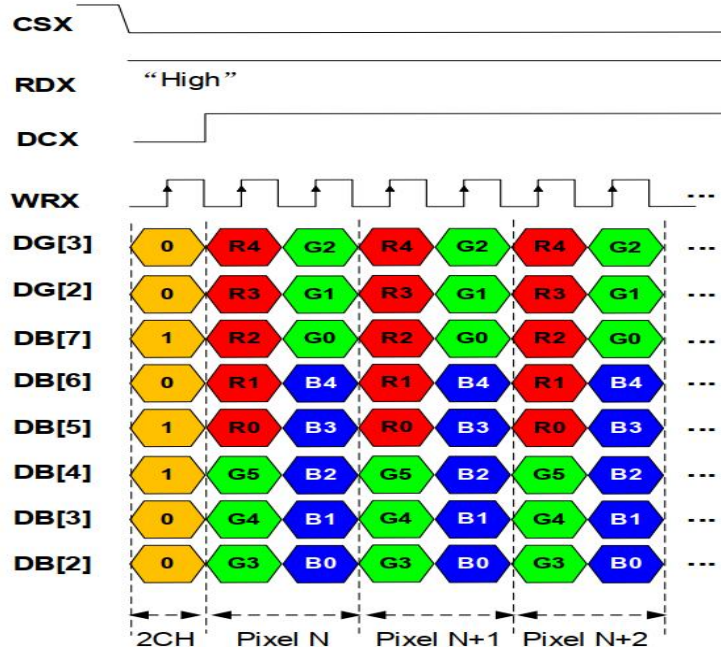


6.3.4. 标准 SPI 4 线 RGB 格式 (6-6-6) Std SPI 4Wire RGB Format (6-6-6)



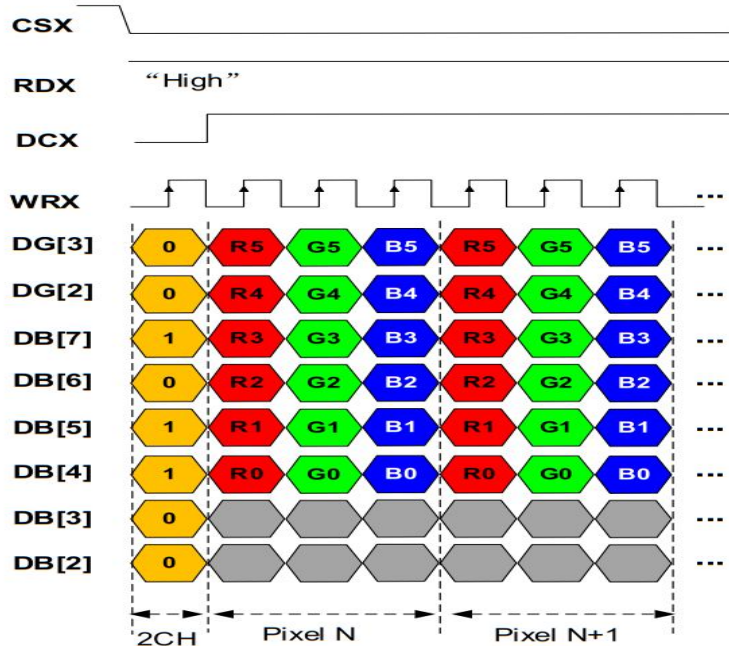
6.3.5. MCU 8 位 RGB 格式 (5-6-5) MCU 8 Bit RGB Format (5-6-5)

<1> IM[2:0] = 3'b000
 <2> Register 41H[1:0] = 2'b01
 <3> Register 3AH = 01h



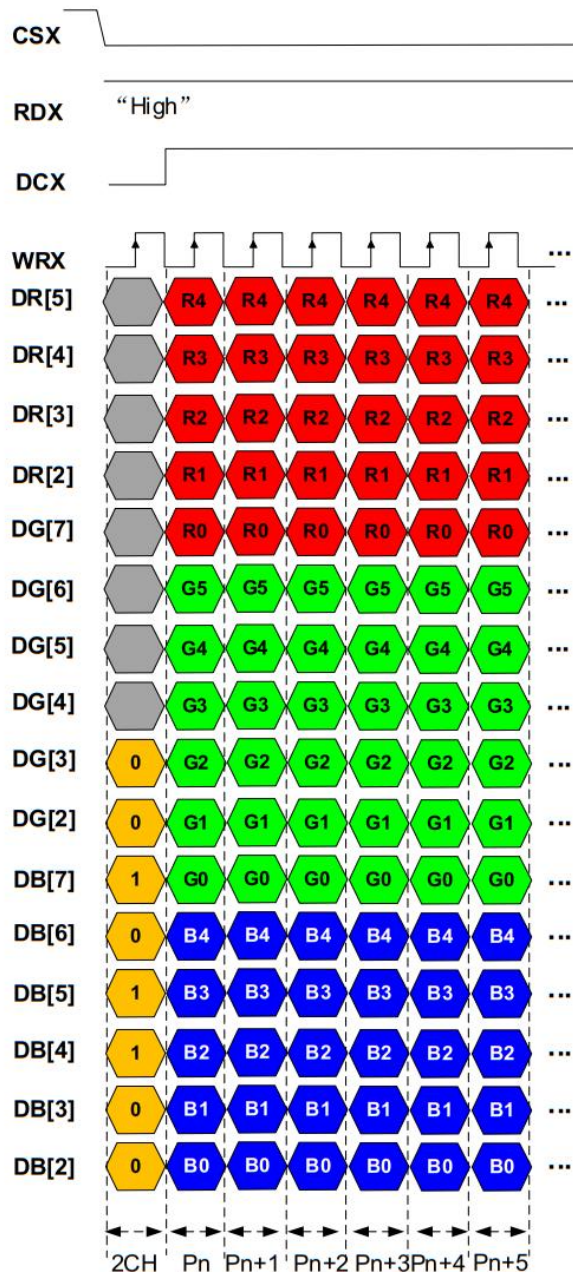
6.3.6. MCU 8 位 RGB 格式 (6-6-6) MCU 8 Bit RGB Format (6-6-6)

<1> IM[2:0] = 3'b000
 <2> Register 41H[1:0] = 2'b01
 <3> Register 3AH = 00h



6.3.7. MCU 16 位 RGB 格式 (5-6-5) MCU 16 Bit RGB Format (5-6-5)

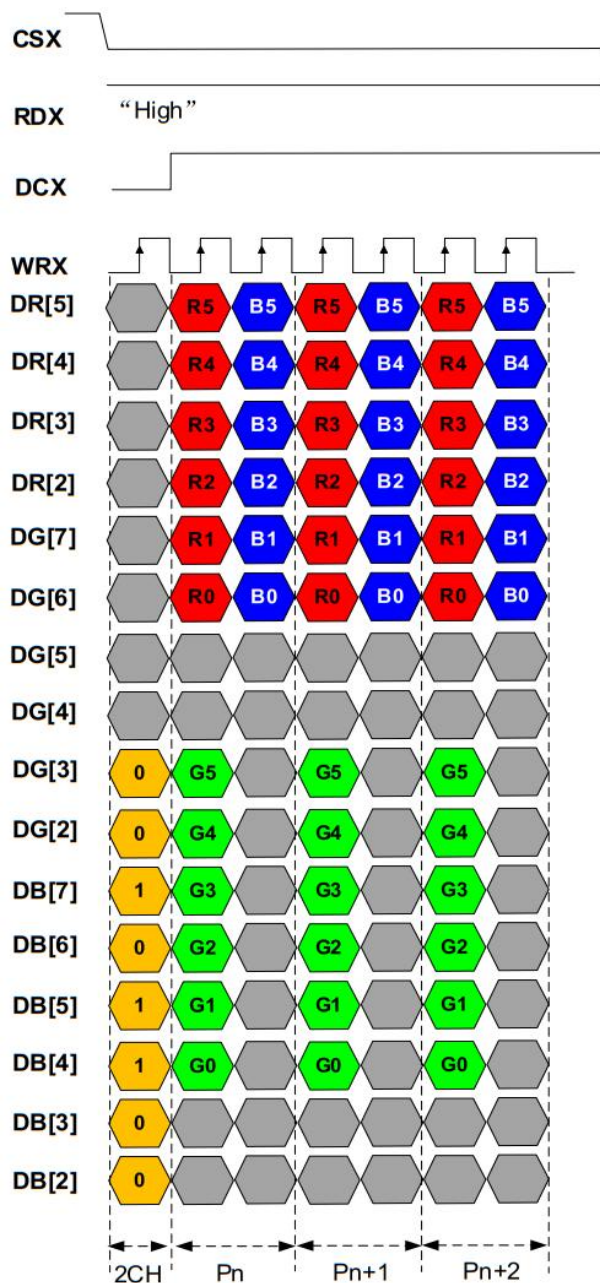
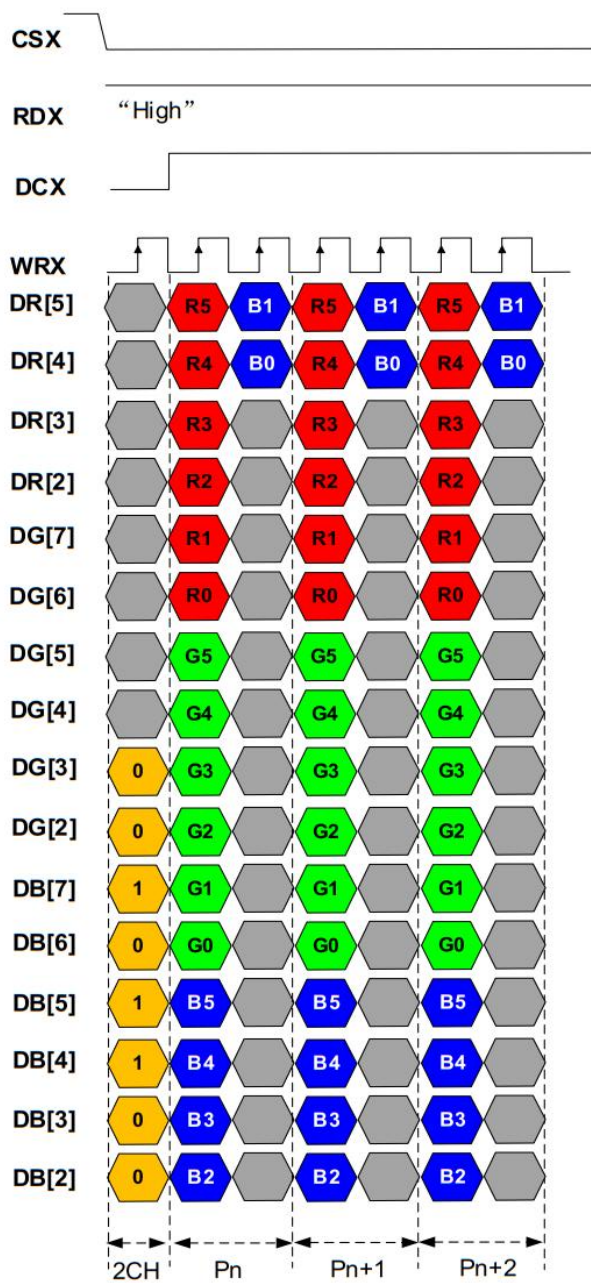
<1> IM[2:0] = 3'b000
 <2> Register 41H[1:0] = 2'b11
 <3> Register 3AH = 01h



6.3.8. MCU 16 位 RGB 格式 (6-6-6) MCU 16 Bit RGB Format (6-6-6)

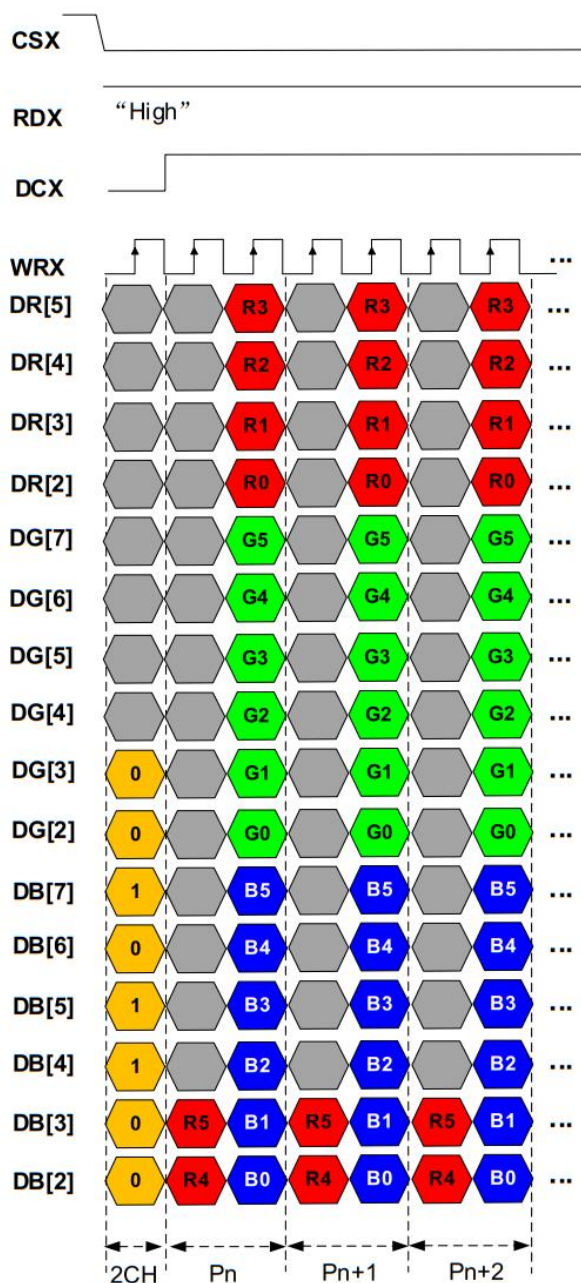
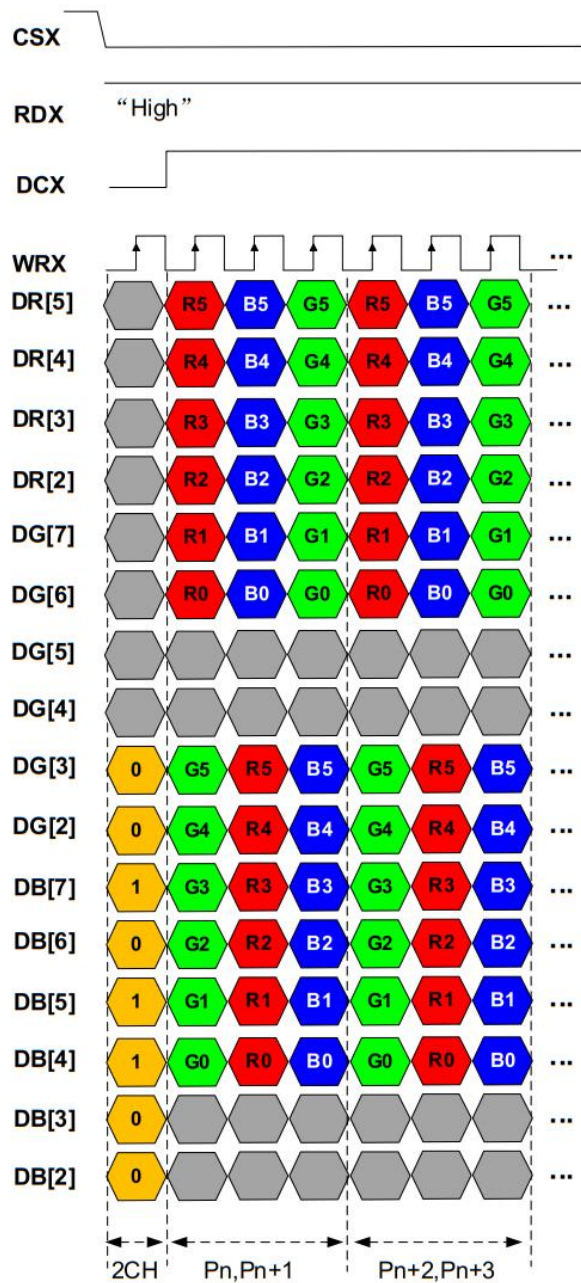
<1> IM[2:0] = 3'b000
 <2> Register 41H = 03h
 <3> Register 3AH = 00h

<1> IM[2:0] = 3'b000
 <2> Register 41H = 13h
 <3> Register 3AH = 00h

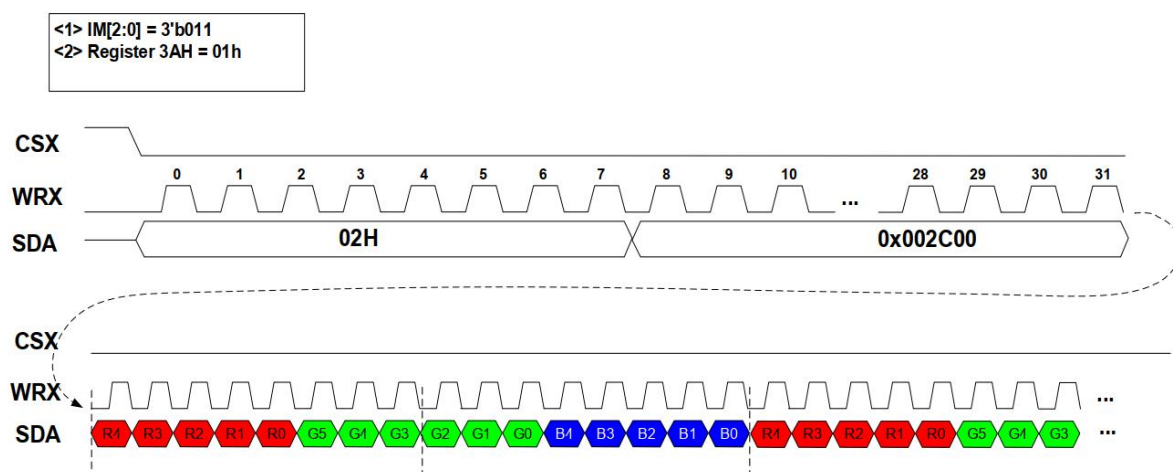


<1> IM[2:0] = 3'b000
 <2> Register 41H = 23h
 <3> Register 3AH = 00h

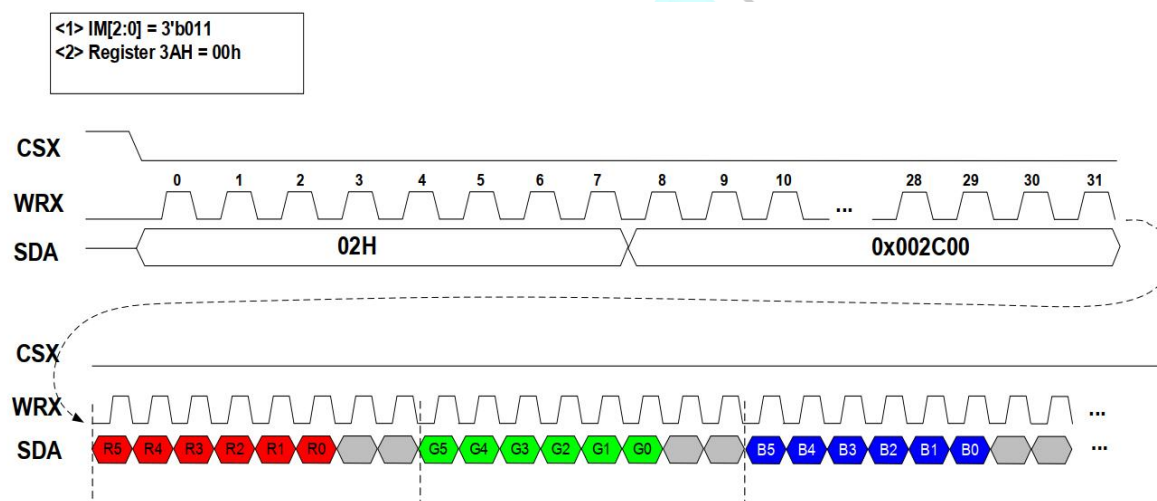
<1> IM[2:0] = 3'b000
 <2> Register 41H = 33h
 <3> Register 3AH = 00h



6.3.9. QSPI 1 通道 RGB 格式 (5-6-5) QSPI 1 lane RGB Format (5-6-5)

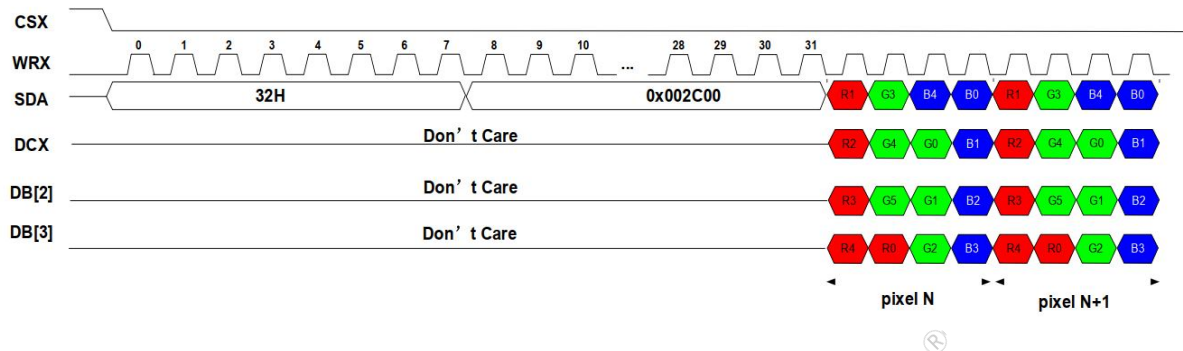


6.3.10. QSPI 1 通道 RGB 格式 (6-6-6) QSPI 1 lane RGB Format (6-6-6)

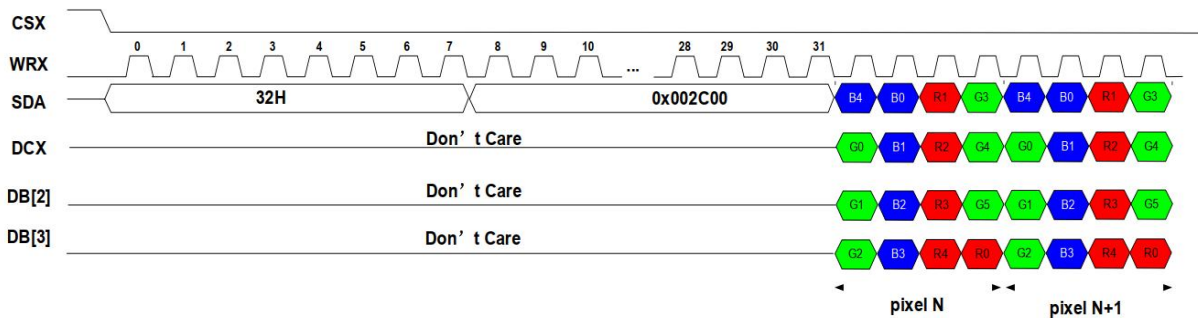


6.3.11. QSPI 4 通道 RGB 格式 (5-6-5) QSPI 4 lane RGB Format (5-6-5)

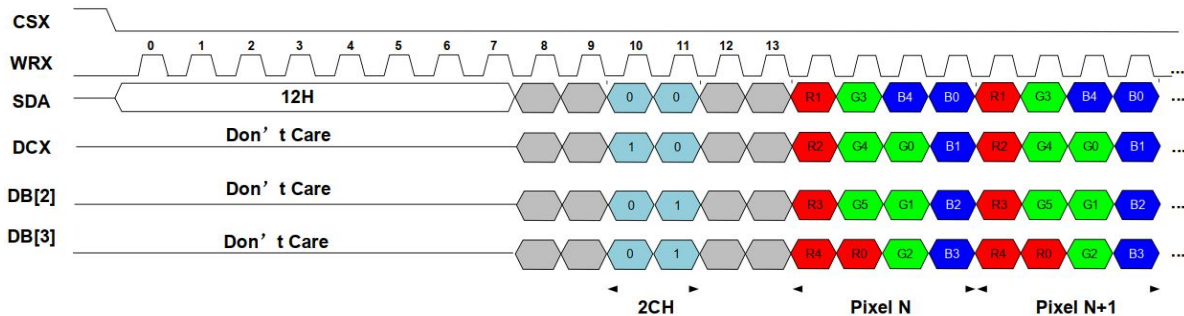
<1> IM[2:0] = 3'b011
<2> Register 3AH = 01h
<3> Register 43H[0] = 1'b0



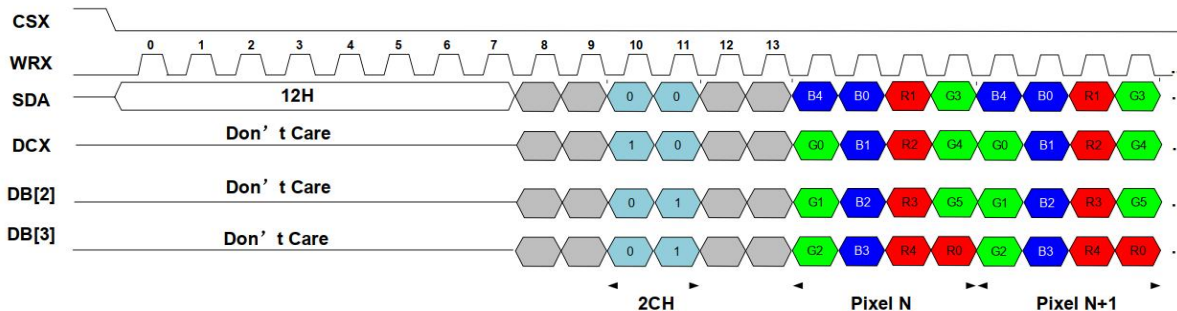
<1> IM[2:0] = 3'b011
<2> Register 3AH = 01h
<3> Register 43H[0] = 1'b1



<1> IM[2:0] = 3'b011
<2> Register 3AH = 01h
<3> Register 43H[0] = 1'b0

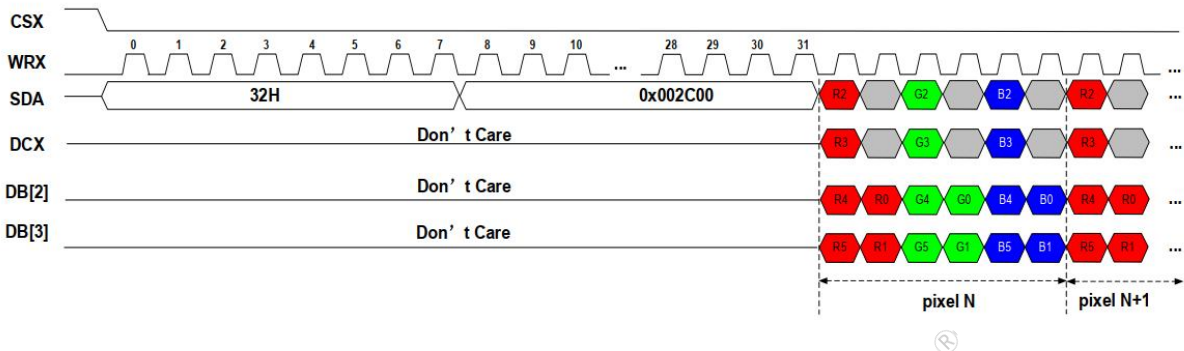


<1> IM[2:0] = 3'b011
<2> Register 3AH = 01h
<3> Register 43H[0] = 1'b1

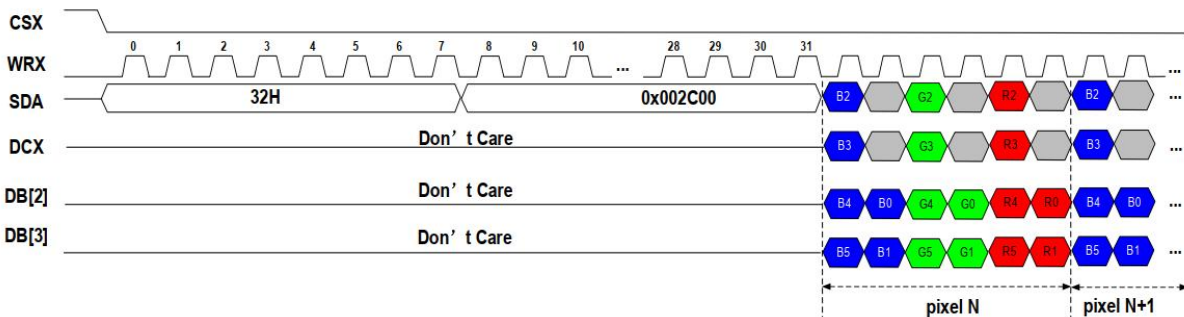


6.3.12. QSPI 4 通道 RGB 格式 (6-6-6) QSPI 4 lane RGB Format (6-6-6)

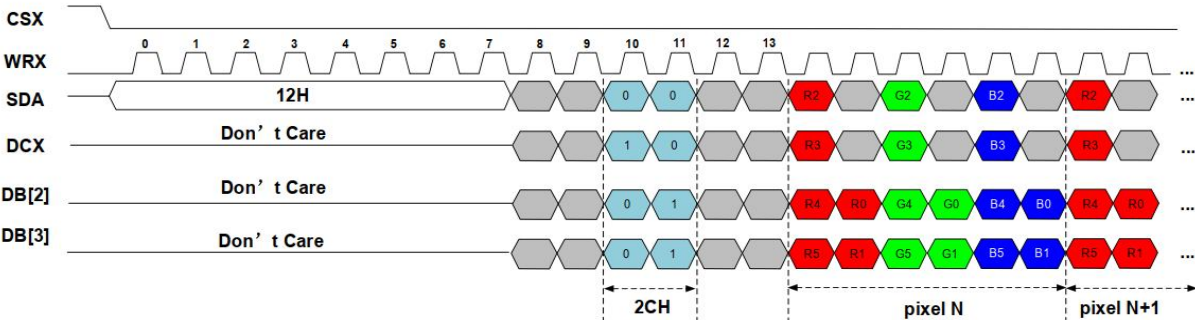
<1> IM[2:0] = 3'b011
 <2> Register 3AH = 00h
 <3> Register 43H[4] = 1'b0



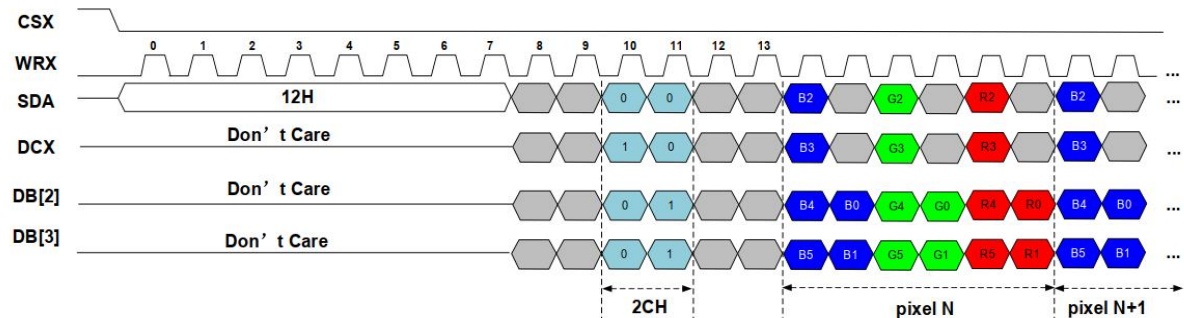
<1> IM[2:0] = 3'b011
 <2> Register 3AH = 00h
 <3> Register 43H[4] = 1'b1



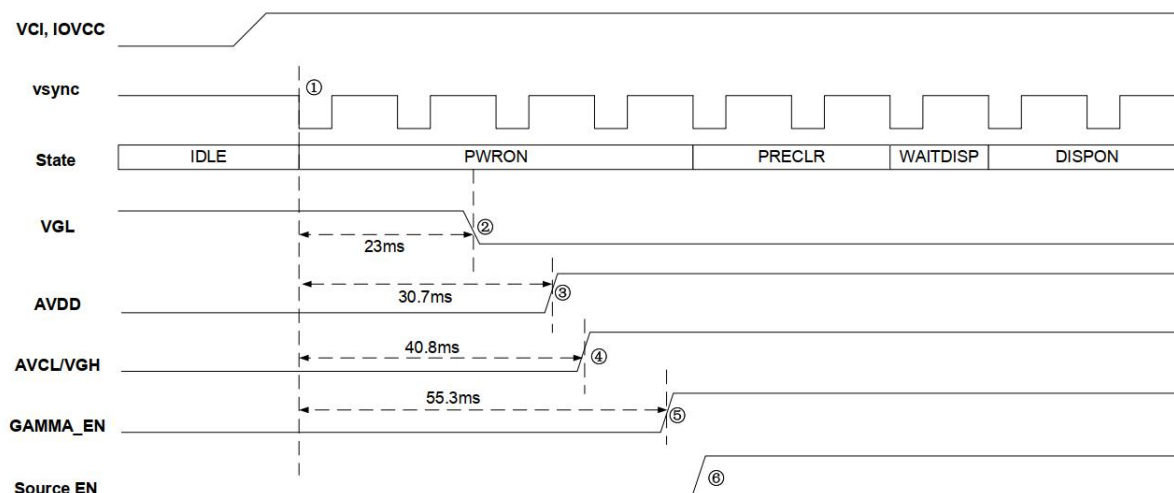
<1> IM[2:0] = 3'b011
 <2> Register 3AH = 00h
 <3> Register 43H[4] = 1'b0



<1> IM[2:0] = 3'b011
 <2> Register 3AH = 00h
 <3> Register 43H[4] = 1'b1



6. 4. 开机上电顺序 Power-on sequence

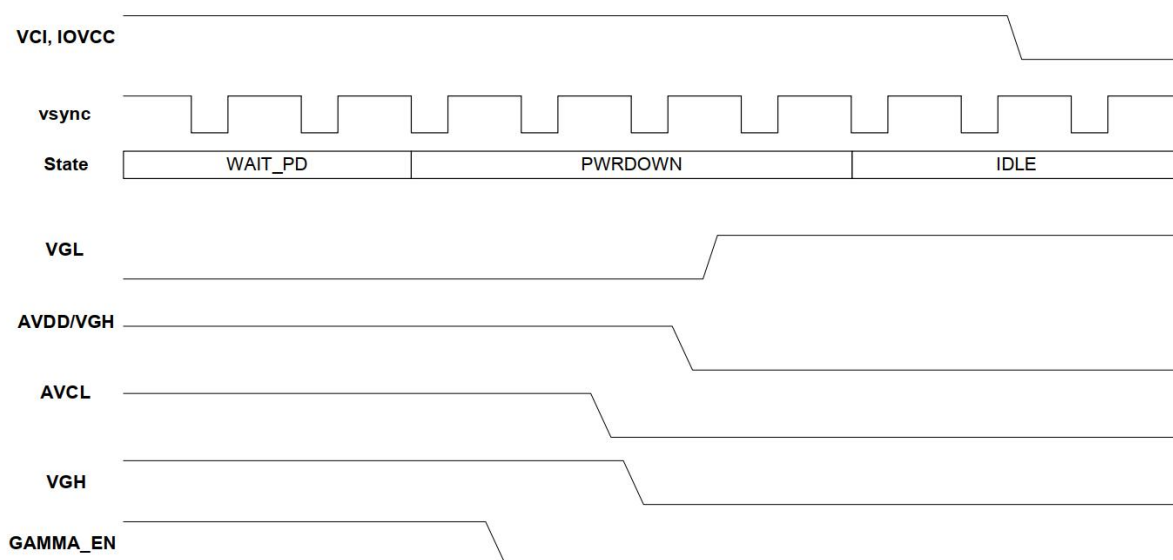


No.	Description	Min. Time
1	After VCI/IOVCC become stable , reset finished and host send command "11H"	Decide by host 11H command
2	VGL from 0V to -11.5V	23ms
3	AVDD from VCI to 6.6V	30.7ms
4	AVCL from 0 to -5V and VGH from AVDD to 15.4	40.8ms
5	Gamma output enable	55.3ms
6	Source output enable	Decide by host "29H" command

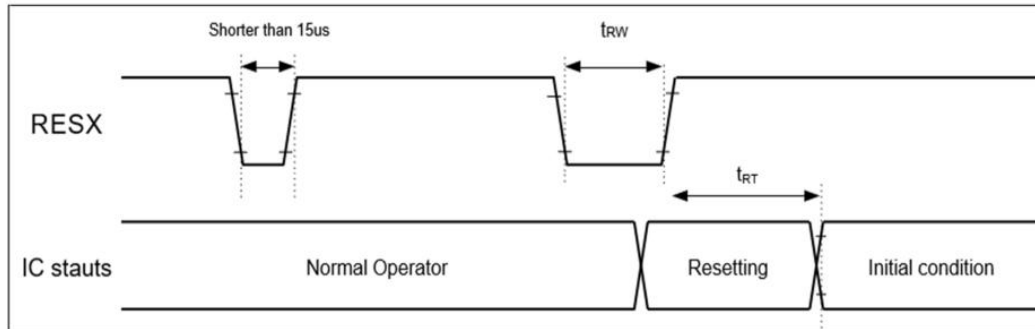
6. 5. 关机下电顺序 Power-off sequence

当主机发送“10H”命令时，状态从 WAIT_PD 到 PWRDOWN，其中电源按顺序禁用。

When host sends “10H” command, State from WAIT_PD to PWRDOWN, in which power disabled sequentially.



6.6. 重置输入定时 Reset input timings



Symbol	Parameter	Related pins	Min.	Max.	Unit
t_{RW}	Reset "L" pulse width ⁽²⁾	RESX	20	-	μ s
t_{RT}	Reset complete time ⁽³⁾	-	-	5 ⁽⁵⁾	ms
		-	-	120 ⁽⁶⁾ (7) (8)	ms

Note:

1. The reset complete time also required time for loading ID bytes from OTP to registers. This loading is done every time when there is HW reset complete time (t_{RT}) within 5 ms after a rising edge of RESX.
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below.

RESX Pulse	Action
Shorter than 15 μ s	Reset Rejected
Longer than 20 μ s	Reset
Between 15 μ s and 20 μ s	Reset Start

3. During the resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out -mode. The display remains the blank state in Sleep In -mode) and then returns to Default condition for H/W reset.
4. Spike Rejection also applies during a valid reset pulse as shown below:

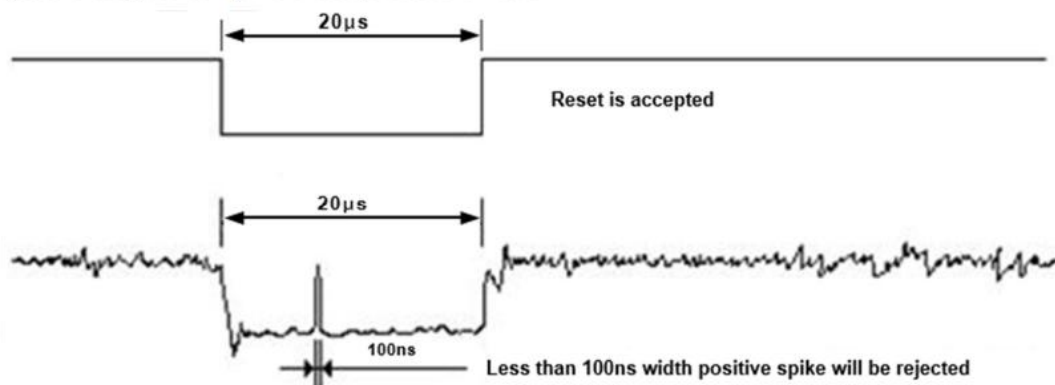


Table 10.4: Reset timings

1. When Reset is applied during Sleep In Mode.
2. When Reset is applied during Sleep Out Mode.
3. is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.
4. After Sleep Out command, it is necessary to wait 120msec then send RESX.

七、可靠性测试 RELIABILITY TEST

7-1 温度和湿度 Temperature and Humidity

测试项目 TEST ITEMS	条件 CONDITIONS	注释 NOTE
高温储存 High Temperature Storage	Ta=+85 °C, 120hrs	
低温储存 Low Temperature Storage	Ta=-30 °C, 120hrs	
高温运行试验 High Temperature Operation	Ta=+85 °C, 120hrs	
低温运行试验 Low Temperature Operation	Ta=-30 °C, 120hrs	
高温高湿（运行测试） High Temperature and High Humidity (Operating)	Ta=+60 °C, 90%RH, 120hrs	

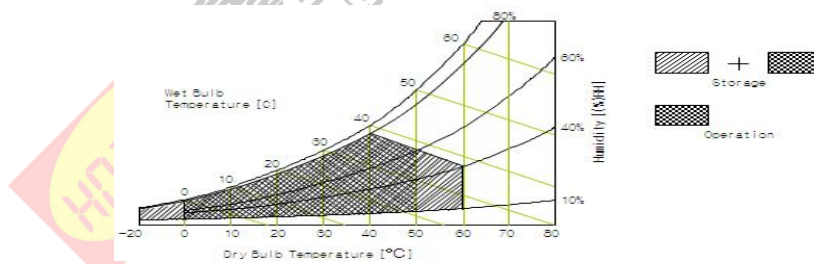
注释 Note:

1. 液晶驱动电压。由于液晶材料的特性，该电压随环境温度而变化。

Liquid Crystal driving voltage. Due to the characteristics of LC Material, this voltage varies with environmental temperature.

2. 温度和相对湿度范围如下图所示。湿球温度最高应为39°C。并且没有冷凝水。

Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.



3. 产品经可靠性测试后，仅保证功能正常，无任何致命缺陷（不显示、线路缺陷、显示异常等）。

After the reliability test, the product only guarantee function normally without any fatal defect(non-display, line defect, abnormal display etc).

4. 所有显示判断均在面板温度恢复到室温2小时后进行

All display judgments are made after the panel temperature returns to room temperature for 2 hours

5. Ta: 环境温度

Ta: Ambient temperature

7-2 冲击和振动 Shock and Vibration

测试项目 TEST ITEMS	条件 CONDITIONS
包装冲击(非操作) Packing Shock (Non-Operation)	<ul style="list-style-type: none"> ● Shock level:980m/s² ● Waveform:1/2 Sine wave,6msec ● ±X, ±Y ±Z, each axis 1 times
包装振动(非操作) Packing Vibration (Non-Operation)	<ul style="list-style-type: none"> ● Frequency range:8-33.3HZ ● Stoke:1.0mm ● Sweep: 10Hz-50Hz ● x, y, z 2 hours for each direction

7-3 静电放电测试 Electrostatic Discharge

测试项目 TEST ITEMS	条件 CONDITIONS
ESD (Non-operation)	150pF, 330 Ω, Contact ±4KV, Air : ±8KV. Note 1 200pF, 0 Ω, ±200V Contact test. Note 2

测量点 Measure Point:

1. LCD玻璃和金属边框
LCD glass and metal bezel
2. 连接器引脚
IF connector pins

八、处理和注意事项 HANDDLING & CAUTIONS

8-1 操作注意事项 Caution For Operation

◆由于液晶模组是玻璃材质，请勿对其施加强烈的机械冲击或静载荷。请小心搬运，因为冲击、振动和粗心的搬运可能会严重影响产品。如果从高处坠落或受到强烈冲击，玻璃可能碎了。

Since the LCM is made of glass, do not apply strong mechanical impact or static load onto it. Handling with care since shock, vibration, and careless handling may seriously affect the product. If it falls from a high place or receives a strong shock, the glass maybe broken.

◆在规定的电压限制内驱动 LCM 是必不可少的，因为高于限制的电压会导致 LCM 的寿命缩短。由直流引起的电化学反应会导致 LCM 出现不良劣化，因此应避免使用直流驱动。

It is indispensable to drive the LCM within the specified voltage limit since the higher voltage than the limit causes LCM's life shorter. An electro-chemical reaction due to DC causes undesirable deterioration of the LCM so that the use of DC drive should avoid.

◆请勿在电源开启时将 LCM 连接到系统或从系统断开连接。

Do not connect or disconnect the LCM to or from the system when power is on.

◆切勿在高温高湿的异常条件下使用 LCM。

Never use the LCM under abnormal conditions of high temperature and high humidity.

◆当暴露于剧烈的温度波动（热到冷或冷到热）时，LCM 可能会受到影响；具体来说，从冷到热的剧烈温度波动会在 LCM 表面产生露水，这可能会影响 LCM 上偏振片的运行。

When expose to drastic fluctuation of temperature(hot to cold or cold to hot), the LCM may be affected; specifically, drastic temperature fluctuation from cold to hot, produces dew on the LCM's surface which may affect the operation of the polarizer on the LCM.

◆在低于工作温度范围的温度下，响应时间将极度延迟，另一方面，在高于其工作范围的温度下，LCM 可能会变黑。然而，这些现象并不意味着 LCM 出现故障或故障。一旦温度恢复到正常运行的推荐温度范围，LCM 将恢复正常运行。

Response time will be extremely delay at lower temperature than the operating temperature range and on the other hand LCM may turn black at temperature above its operational range. However those phenomenon do not mean malfunction or out of order with the LCM. The LCM will revert to normal operation once the temperature returns to the recommended temperature range for normal operation.

◆为防止LCD产生图像残留，在使用常黑面板时不要长时间显示固定图案。如果LCD需要显示固定图案，建议时间少于 2 分钟或者更短的时间内刷新显示内容一次或多次。强烈建议使用黑色图像或移动图像作为屏幕保护程序。

In order to prevent the LCD from producing image retention, do not display a fixed pattern for a long time when using a normally black panel. If the LCD needs to display a fixed pattern, it is recommended to refresh the display one or more times in less

than 2 minutes or less. It is strongly recommended to use a black image or moving image as a screen saver.

◆强烈的光照会导致 LCD 偏光片和彩色滤光片退化。 不准在强光或高温高湿下长期存放或直接运行。

Strong sunlight can cause LCD polarizers and color filters to degrade. Long-term storage or direct operation under strong light or high temperature and humidity is not allowed.

8-2 防静电措施 Caution Against Static Charge

◆LCM 使用 C-MOS LSI 驱动器，因此建议客户将任何未使用的输入端连接到 Vdd 或 Vss，上电前不要输入任何信号，并将您的身体、工作/装配区、装配设备接地 防止静电。

The LCM use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipments to protect against static electricity.

◆缓慢去除保护膜，保持去除方向与面板表面不垂直约 30 度，如有可能，在离子风机等 ESD 控制装置下，工作室湿度应保持在 50%RH 以上，以减少静电风险

Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, if possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.

◆避免使用合成纤维制成的工作服。 我们推荐棉质衣服或其他经过导电处理的纤维。

Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

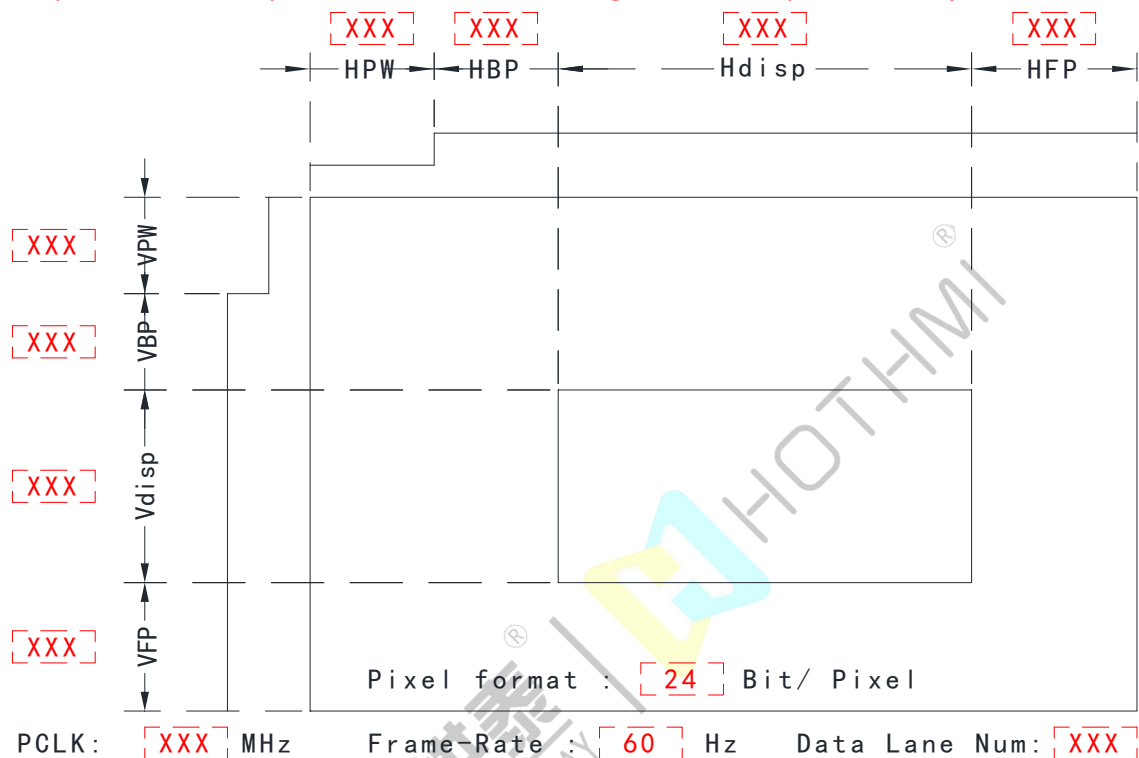
◆在处理 LCM 时，请戴上不带电材料的手套。 对地导电手腕和对地导电鞋是必需的

In handling the LCM, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary

九、初始化代码 Initialization code

说明：这些参数值仅供参考，可能并不完全适用于所有软件平台，
请根据您使用的平台优化参数。

Description : These parameter values are for reference only and
may not be fully applicable to all software platforms, please
optimise the parameters according to the platform you are using.



```
Void Panel_Initial_code(void)
```

```
{
//=====上电操作顺序 Power on operation sequence=====//
Set_STBYB(0); // GPI02 SET STBYB low
Set_POWER(1, 0, 0, 0); // 1.8V ON, 2.8V OFF, 5V OFF, BL OFF
Delay(150);
Set_POWER(1, 1, 1, 0); // 1.8V ON, 2.8V ON, 5V ON, BL OFF
Delay(150);
Set_RESET(1); // MIPI RESET 1, LCD RESET 1
Delay(200);
Set_RESET(0); // MIPI RESET 0, LCD RESET 0
Delay(100);
Set_RESET(1); // MIPI RESET 1, LCD RESET 1
Delay(100);
//=====初始化代码 initialization code=====//
```

//SPI, MCU 接口初始化代码共用 SPI, MCU interface initialisation code share

//-----Start Initial Code -----//

```
WriteComm(0xff);
WriteData(0xa5);
WriteComm(0xE7); //TE_output_en
WriteData(0x10);
WriteComm(0x35); //TE_ interface_en
WriteData(0x01);
WriteComm(0x3A);
WriteData(0x01); //00---666//01---565
WriteComm(0x40);
WriteData(0x01);
WriteComm(0x41);
WriteData(0x03); //01---8bit//03---16bit
WriteComm(0x55);
WriteData(0x01);
WriteComm(0x44);
WriteData(0x15);
WriteComm(0x45);
WriteData(0x15);
WriteComm(0x7d);
WriteData(0x03);
WriteComm(0xc1);
WriteData(0xab);
WriteComm(0xc2);
WriteData(0x17);
WriteComm(0xc3);
WriteData(0x10);
WriteComm(0xc6);
WriteData(0x3a);
WriteComm(0xc7);
WriteData(0x25);
WriteComm(0xc8);
WriteData(0x11);
WriteComm(0x7a);
WriteData(0x49);
WriteComm(0x6f);
WriteData(0x2f);
WriteComm(0x78);
WriteData(0x4b);
```

WriteComm (0x73) ;
WriteData (0x08) ;
WriteComm (0x74) ;
WriteData (0x12) ;
WriteComm (0xc9) ;
WriteData (0x00) ;
WriteComm (0x67) ;
WriteData (0x11) ;

WriteComm (0x51) ;
WriteData (0x20) ;
WriteComm (0x52) ;
WriteData (0x7c) ;
WriteComm (0x53) ;
WriteData (0x1c) ;
WriteComm (0x54) ;
WriteData (0x77) ;

WriteComm (0x46) ;
WriteData (0x0a) ;
WriteComm (0x47) ;
WriteData (0x2a) ;
WriteComm (0x48) ;
WriteData (0x0a) ;
WriteComm (0x49) ;
WriteData (0x1a) ;

WriteComm (0x56) ;
WriteData (0x43) ;
WriteComm (0x57) ;
WriteData (0x42) ;
WriteComm (0x58) ;
WriteData (0x3c) ;
WriteComm (0x59) ;
WriteData (0x64) ;
WriteComm (0x5a) ;
WriteData (0x41) ;
WriteComm (0x5b) ;
WriteData (0x3c) ;
WriteComm (0x5c) ;

WriteData (0x02) ;
WriteComm (0x5d) ;
WriteData (0x3c) ;
WriteComm (0x5e) ;
WriteData (0x1f) ;
WriteComm (0x60) ;
WriteData (0x80) ;
WriteComm (0x61) ;
WriteData (0x3f) ;
WriteComm (0x62) ;
WriteData (0x21) ;
WriteComm (0x63) ;
WriteData (0x07) ;
WriteComm (0x64) ;
WriteData (0xe0) ;
WriteComm (0x65) ;
WriteData (0x01) ;
WriteComm (0xca) ;
WriteData (0x20) ;
WriteComm (0xcb) ;
WriteData (0x52) ;
WriteComm (0xcc) ;
WriteData (0x10) ;
WriteComm (0xcd) ;
WriteData (0x42) ;

WriteComm (0xD0) ;
WriteData (0x20) ;
WriteComm (0xD1) ;
WriteData (0x52) ;
WriteComm (0xD2) ;
WriteData (0x10) ;
WriteComm (0xD3) ;
WriteData (0x42) ;
WriteComm (0xD4) ;
WriteData (0x0a) ;
WriteComm (0xD5) ;
WriteData (0x32) ;
WriteComm (0xe5) ;
WriteData (0x06) ;

```
WriteComm(0xe6);  
WriteData(0x00);  
WriteComm(0x6e);  
WriteData(0x14);  
//gamma 01  
WriteComm(0x80);  
WriteData(0x04);  
WriteComm(0xA0);  
WriteData(0x00);  
WriteComm(0x81);  
WriteData(0x07);  
WriteComm(0xA1);  
WriteData(0x05);  
WriteComm(0x82);  
WriteData(0x06);  
WriteComm(0xA2);  
WriteData(0x04);  
WriteComm(0x83);  
WriteData(0x39);  
WriteComm(0xA3);  
WriteData(0x39);  
WriteComm(0x84);  
WriteData(0x3a);  
WriteComm(0xA4);  
WriteData(0x3a);  
WriteComm(0x85);  
WriteData(0x3f);  
WriteComm(0xA5);  
  
WriteData(0x3f);  
WriteComm(0x86);  
WriteData(0x2c);  
WriteComm(0xA6);  
WriteData(0x2a);  
WriteComm(0x87);  
WriteData(0x43);  
WriteComm(0xA7);  
WriteData(0x47);  
WriteComm(0x88);  
WriteData(0x08);
```

```
WriteComm(0xA8);  
WriteData(0x08);  
WriteComm(0x89);  
WriteData(0x0f);  
WriteComm(0xA9);  
WriteData(0x0f);  
WriteComm(0x8a);  
WriteData(0x17);  
WriteComm(0xAa);  
WriteData(0x17);  
WriteComm(0x8b);  
WriteData(0x10);  
WriteComm(0xAb);  
WriteData(0x10);  
WriteComm(0x8c);  
WriteData(0x16);  
WriteComm(0xAc);  
WriteData(0x16);  
WriteComm(0x8d);  
WriteData(0x14);  
WriteComm(0xAd);  
WriteData(0x14);  
WriteComm(0x8e);  
WriteData(0x11);  
WriteComm(0xAe);  
WriteData(0x11);  
WriteComm(0x8f);  
WriteData(0x14);  
WriteComm(0xAf);  
WriteData(0x14);
```

```
WriteComm(0x90);  
WriteData(0x06);  
WriteComm(0xB0);  
WriteData(0x06);  
WriteComm(0x91);  
WriteData(0x0f);  
WriteComm(0xB1);  
WriteData(0x0f);  
WriteComm(0x92);
```

```
WriteData(0x16);
WriteComm(0xB2);
WriteData(0x16);
WriteComm(0xff);
WriteData(0x00);
WriteComm(0x11);
Delays (200);
WriteComm(0x29);
Delays (120);
```

```
Delay(150);
Set_STBYB(1); // GPIO2 SET STBYB High
Set_POWER(1, 1, 1, 1); // 1.8V ON, 2.8V ON, 5V ON, BL ON
Delay(150);
}
```

MIPI 代码格式说明 Code Format Description

```
SSD_SEND(0x01, 0xCC, 0x15, 0x04, 0x35);
```

参数 1 (Parameter 1) :

- 0x00 DCS_Short_Write
- 0x01 DCS_Long_Write
- 0x10 Generic_Short_Write
- 0x11 Generic_Long_Write

参数 2 (Parameter 2) : 寄存器地址(Register Address)

参数 3 ~ N (Parameter 3 ~ N) : 寄存器数据 (Register Data)

```
DCS_Short_Write_NP(0x11);
```

参数 1 (Parameter 1) : 寄存器地址, 无数据 (Register Address, no data)

文档修订记录

Document revision history :

版本 Version	日期 DATE	修改说明 Modify description	编辑 Editorial
2-0	2026-10-28	初次编制 First compilation.	R. L

— — 结束 — —

— — END — —